

# Model Name: GA-X79-UD7 Rev. 1.01

SHEET TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA2011-A
05	CPU_LGA2011-B
06	CPU_LGA2011-C
07~08	DDR III CHANNEL A/B
09~10	DDR III CHANNEL C/D
11	PCH_SATA_SAS_GPIO_AUDIO
12	PCH_DMI_USB_PCIE_PCI
13	PCH_PWR_GND
14	PCI EXPRESS X16 SLOT_1
15	PCI EXPRESS X16 SLOT SWITCH_1
16	PCI EXPRESS X8 SLOT_1
17	PCI EXPRESS X16 SLOT_2
18	PCI EXPRESS X16 SLOT SWITCH_2
19	PCI EXPRESS X8 SLOT_2
20	PCI EXPRESS X4 SLOT_1
21	PCI EXPRESS X4 SLOT_2
22	PCI EXPRESS X1 / DUAL BIOS
23	VCORE ISL6367
24	VCCSA PHASE
25	VCORE PHASE 1~4
26	VCORE PHASE 5~8
27	VCORE PHASE 9~12

SHEET

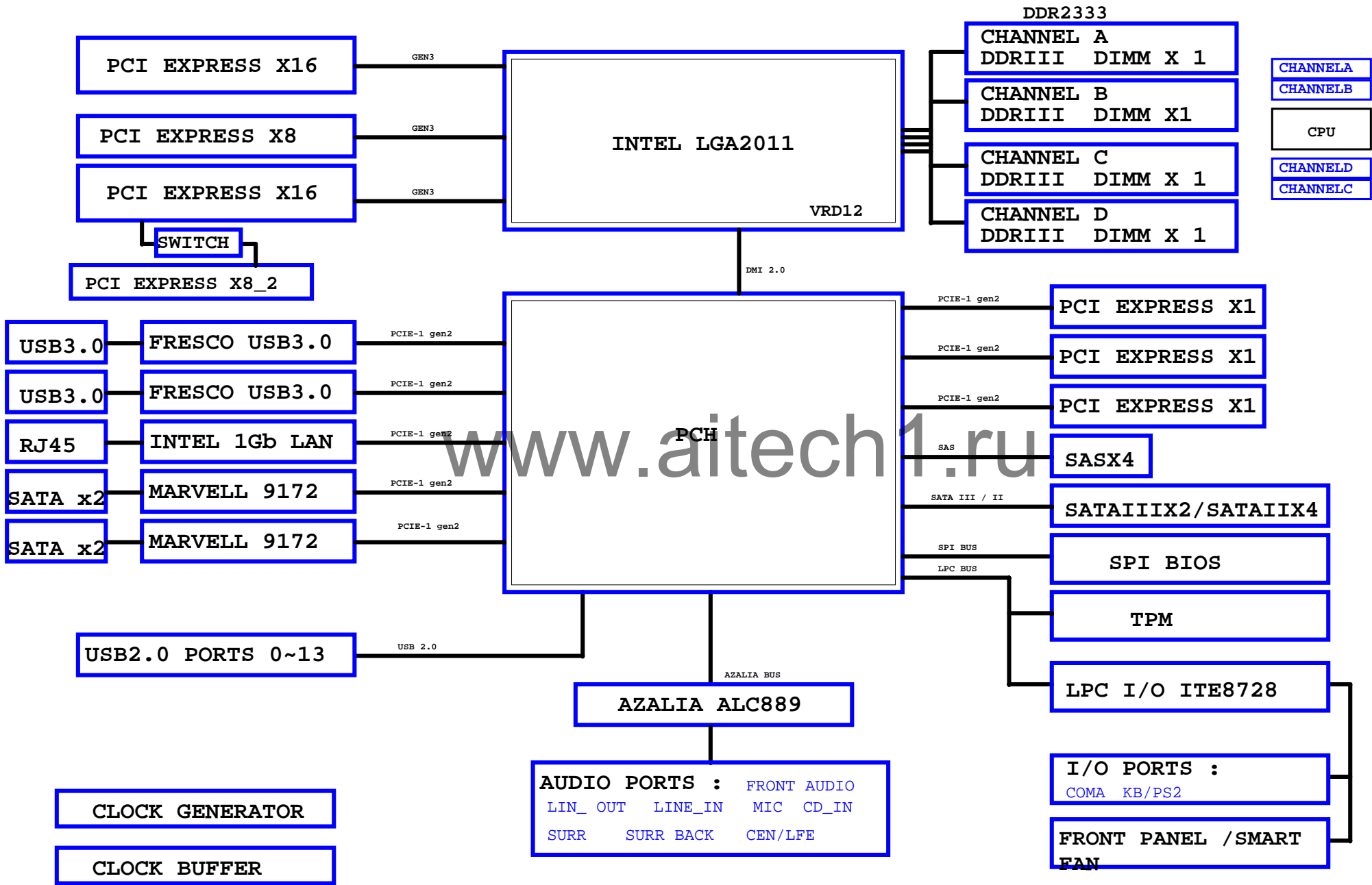
TITLE

28	VCORE PHASE 13~16
29	VCORE IT8275
30	ISL6322_CPU_VTTD
31	DDR CH A/B POWER
32	DDR CH C/D POWER
33	PCH CORE
34	DISCRETE POWER 1
35	DISCRETE POWER 2
36	-PROHOT,KB/MS,RUSB
37	FP ,FUSB ,FDD
38	ATX
39	HWM ,FAN CTRL
40	SYSTEM FAN
41	CLOCK GEN.
42	Clock buffer
43	ITE 8728
44	ALC898
45	REAR AUDIO JACK
46	R_USB3_FL1009
47	R_USB3_CONNECTOR
48	F_USB3_FL1009
49	OVER CLOCK / 80 PORT
50	Highly Switch
51	LEWISVILLE_82579 LAN
52	PCH GPIO LIST

<b>GIGABYTE™</b>			
Title <b>Cover Sheet</b>			
Size	Document Number	Rev	
Custom	<b>GA-X79-UD7</b>	<b>1.01</b>	
Date:	Tuesday, October 04, 2011	Sheet	1 of 54

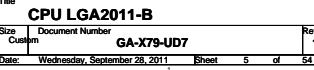


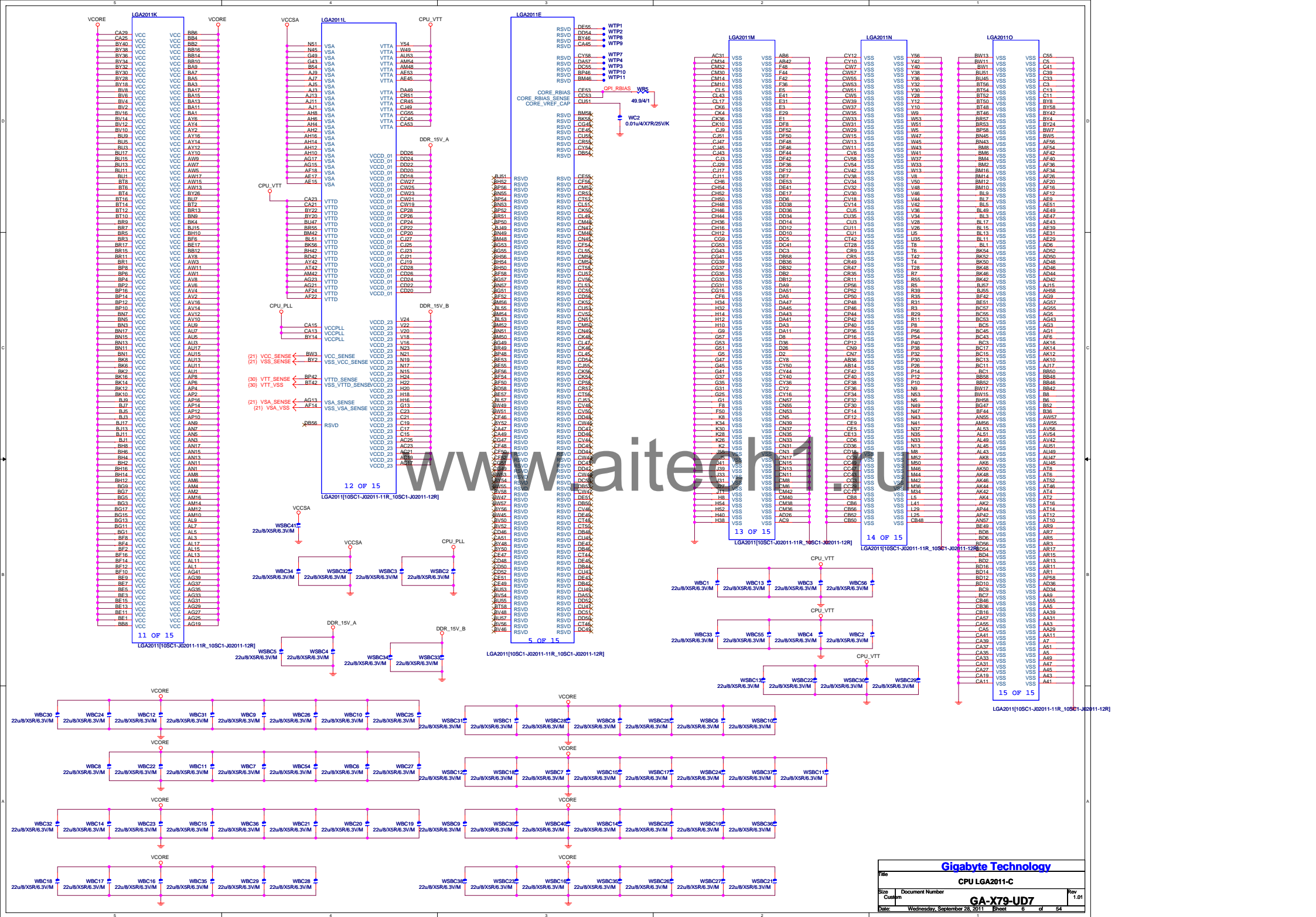
BLOCK DIAGRAM



LGA2011F									
M_AA00	CL25	DDR0_MA00	DDR0_DQ00	CC7	M_DA0				
M_AA01	CL26	DDR0_MA01	DDR0_DQ01	CC8	M_DA1				
M_AA02	CL27	DDR0_MA02	DDR0_DQ02	CC9	M_DA2				
M_AA03	CL28	DDR0_MA03	DDR0_DQ03	CC10	M_DA3				
M_AA04	CL29	DDR0_MA04	DDR0_DQ04	CC11	M_DA4				
M_AA05	CL30	DDR0_MA05	DDR0_DQ05	CC12	M_DA5				
M_AA06	CL31	DDR0_MA06	DDR0_DQ06	CC13	M_DA6				
M_AA07	CL32	DDR0_MA07	DDR0_DQ07	CC14	M_DA7				
M_AA08	CL33	DDR0_MA08	DDR0_DQ08	CC15	M_DA8				
M_AA09	CL34	DDR0_MA09	DDR0_DQ09	CC16	M_DA9				
M_AA10	CL35	DDR0_MA10	DDR0_DQ10	CC17	M_DA10				
M_AA11	CL36	DDR0_MA11	DDR0_DQ11	CC18	M_DA11				
M_AA12	CL37	DDR0_MA12	DDR0_DQ12	CC19	M_DA12				
M_AA13	CL38	DDR0_MA13	DDR0_DQ13	CC20	M_DA13				
M_AA14	CL39	DDR0_MA14	DDR0_DQ14	CC21	M_DA14				
M_AA15	CL40	DDR0_MA15	DDR0_DQ15	CC22	M_DA15				
(7) M_SBA00 <-> CC23									
(7) M_SBA01 <-> CC24									
(7) M_SBA02 <-> CC25									
(7) M_SBA03 <-> CC26									
(7) M_SBA04 <-> CC27									
(7) M_SBA05 <-> CC28									
(7) M_SBA06 <-> CC29									
(7) M_SBA07 <-> CC30									
(7) M_SBA08 <-> CC31									
(7) M_SBA09 <-> CC32									
(7) M_SBA10 <-> CC33									
(7) M_SBA11 <-> CC34									
(7) M_SBA12 <-> CC35									
(7) M_SBA13 <-> CC36									
(7) M_SBA14 <-> CC37									
(7) M_SBA15 <-> CC38									
(7) M_SBA16 <-> CC39									
(7) M_SBA17 <-> CC40									
(7) M_SBA18 <-> CC41									
(7) M_SBA19 <-> CC42									
(7) M_SBA20 <-> CC43									
(7) M_SBA21 <-> CC44									
(7) M_SBA22 <-> CC45									
(7) M_SBA23 <-> CC46									
(7) M_SBA24 <-> CC47									
(7) M_SBA25 <-> CC48									
(7) M_SBA26 <-> CC49									
(7) M_SBA27 <-> CC50									
(7) M_SBA28 <-> CC51									
(7) M_SBA29 <-> CC52									
(7) M_SBA30 <-> CC53									
(7) M_SBA31 <-> CC54									
(7) M_SBA32 <-> CC55									
(7) M_SBA33 <-> CC56									
(7) M_SBA34 <-> CC57									
(7) M_SBA35 <-> CC58									
(7) M_SBA36 <-> CC59									
(7) M_SBA37 <-> CC60									
(7) M_SBA38 <-> CC61									
(7) M_SBA39 <-> CC62									
(7) M_SBA40 <-> CC63									
(7) M_SBA41 <-> CC64									
(7) M_SBA42 <-> CC65									
(7) M_SBA43 <-> CC66									
(7) M_SBA44 <-> CC67									
(7) M_SBA45 <-> CC68									
(7) M_SBA46 <-> CC69									
(7) M_SBA47 <-> CC70									
(7) M_SBA48 <-> CC71									
(7) M_SBA49 <-> CC72									
(7) M_SBA50 <-> CC73									
(7) M_SBA51 <-> CC74									
(7) M_SBA52 <-> CC75									
(7) M_SBA53 <-> CC76									
(7) M_SBA54 <-> CC77									
(7) M_SBA55 <-> CC78									
(7) M_SBA56 <-> CC79									
(7) M_SBA57 <-> CC80									
(7) M_SBA58 <-> CC81									
(7) M_SBA59 <-> CC82									
(7) M_SBA60 <-> CC83									
(7) M_SBA61 <-> CC84									
(7) M_SBA62 <-> CC85									
(7) M_SBA63 <-> CC86									
(7) M_SBA64 <-> CC87									
(7) M_SBA65 <-> CC88									
(7) M_SBA66 <-> CC89									
(7) M_SBA67 <-> CC90									
(7) M_SBA68 <-> CC91									
(7) M_SBA69 <-> CC92									
(7) M_SBA70 <-> CC93									
(7) M_SBA71 <-> CC94									
(7) M_SBA72 <-> CC95									
(7) M_SBA73 <-> CC96									
(7) M_SBA74 <-> CC97									
(7) M_SBA75 <-> CC98									
(7) M_SBA76 <-> CC99									
(7) M_SBA77 <-> CC100									
(7) M_SBA78 <-> CC101									
(7) M_SBA79 <-> CC102									
(7) M_SBA80 <-> CC103									
(7) M_SBA81 <-> CC104									
(7) M_SBA82 <-> CC105									
(7) M_SBA83 <-> CC106									
(7) M_SBA84 <-> CC107									
(7) M_SBA85 <-> CC108									
(7) M_SBA86 <-> CC109									
(7) M_SBA87 <-> CC110									
(7) M_SBA88 <-> CC111									
(7) M_SBA89 <-> CC112									
(7) M_SBA90 <-> CC113									
(7) M_SBA91 <-> CC114									
(7) M_SBA92 <-> CC115									
(7) M_SBA93 <-> CC116									
(7) M_SBA94 <-> CC117									
(7) M_SBA95 <-> CC118									
(7) M_SBA96 <-> CC119									
(7) M_SBA97 <-> CC120									
(7) M_SBA98 <-> CC121									
(7) M_SBA99 <-> CC122									
(7) M_SBA100 <-> CC123									











## Gigabyte Technology

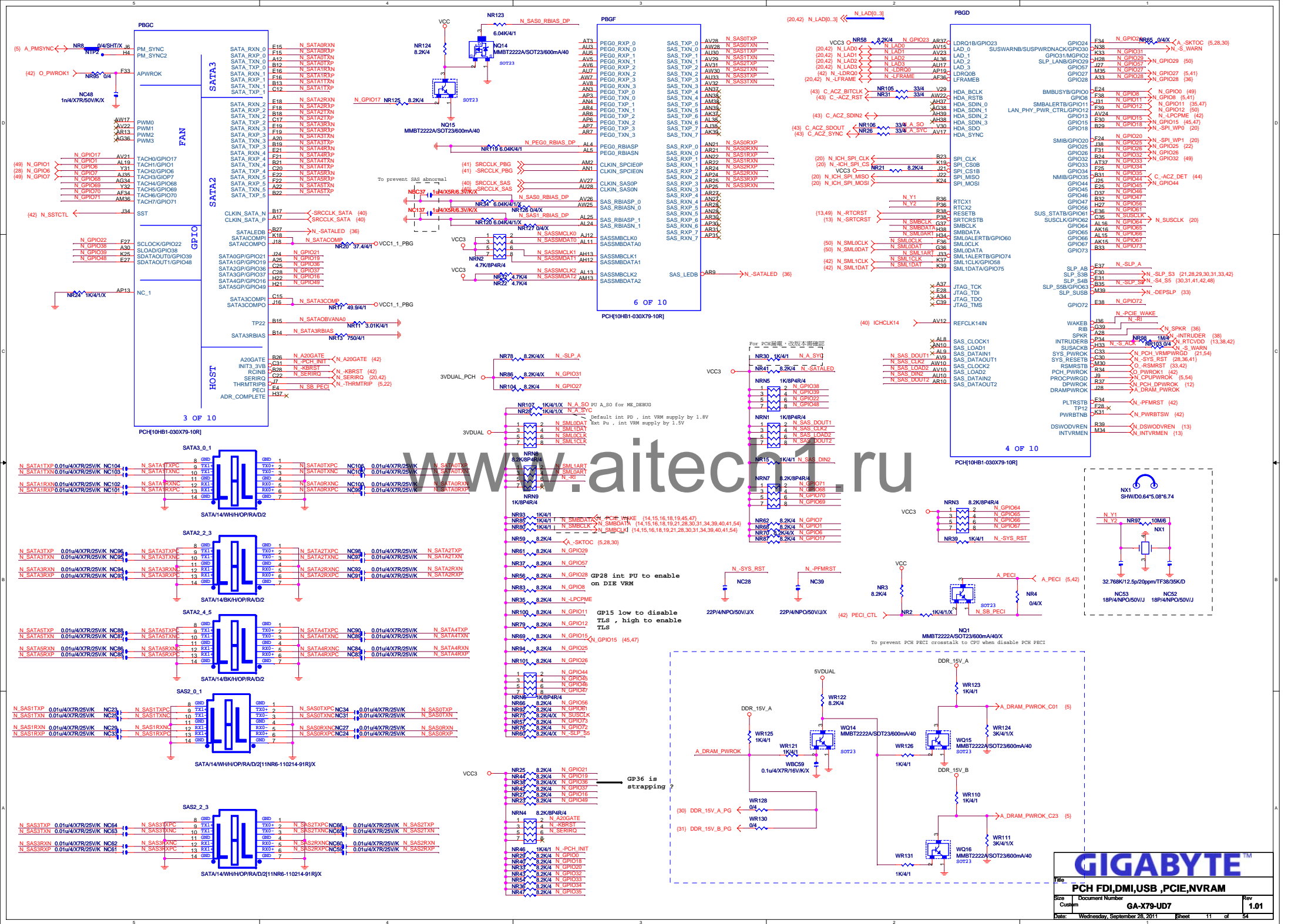
CHANNEL B  
SA2:0=100

CHANNEL B  
SA2:0=101



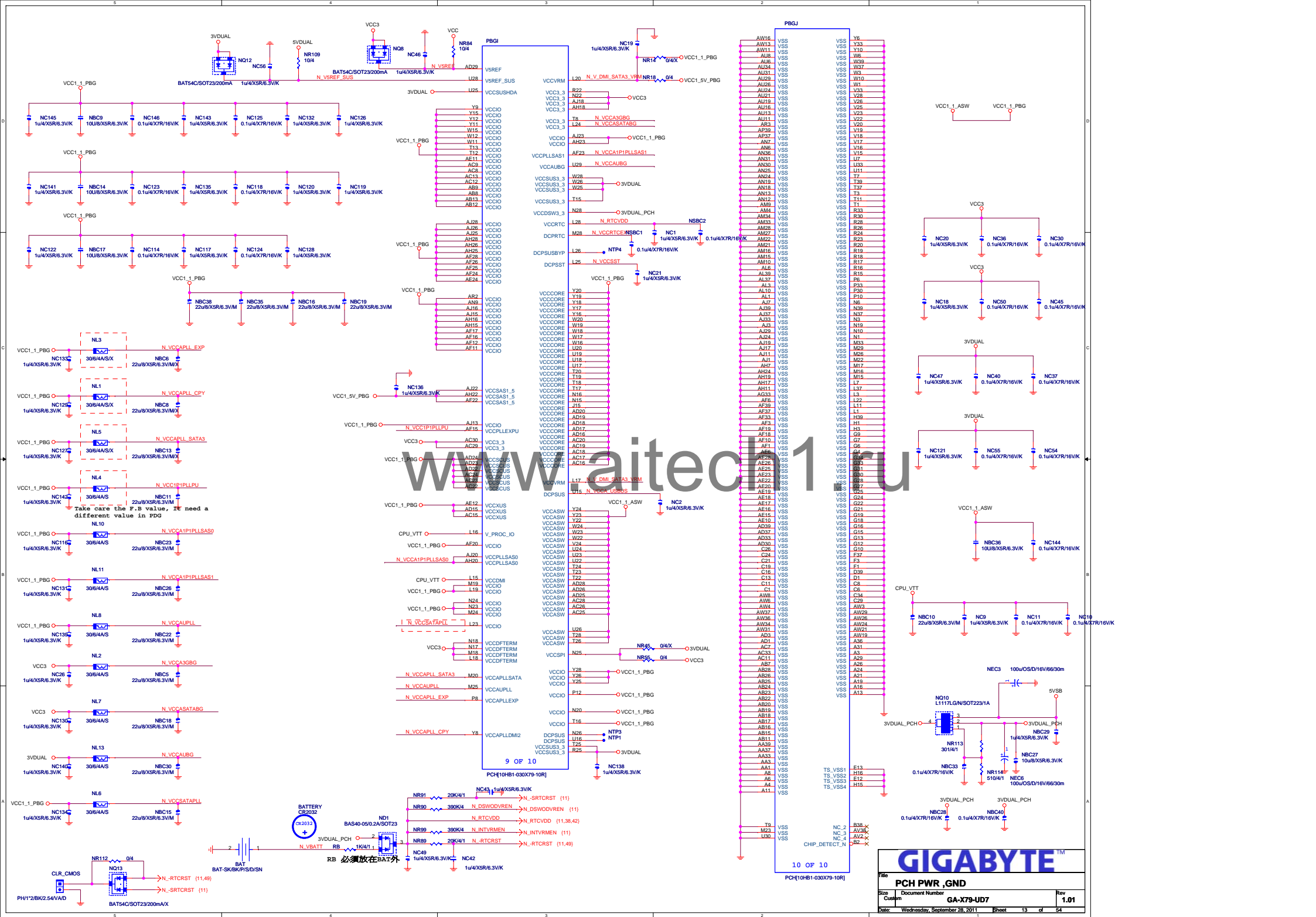


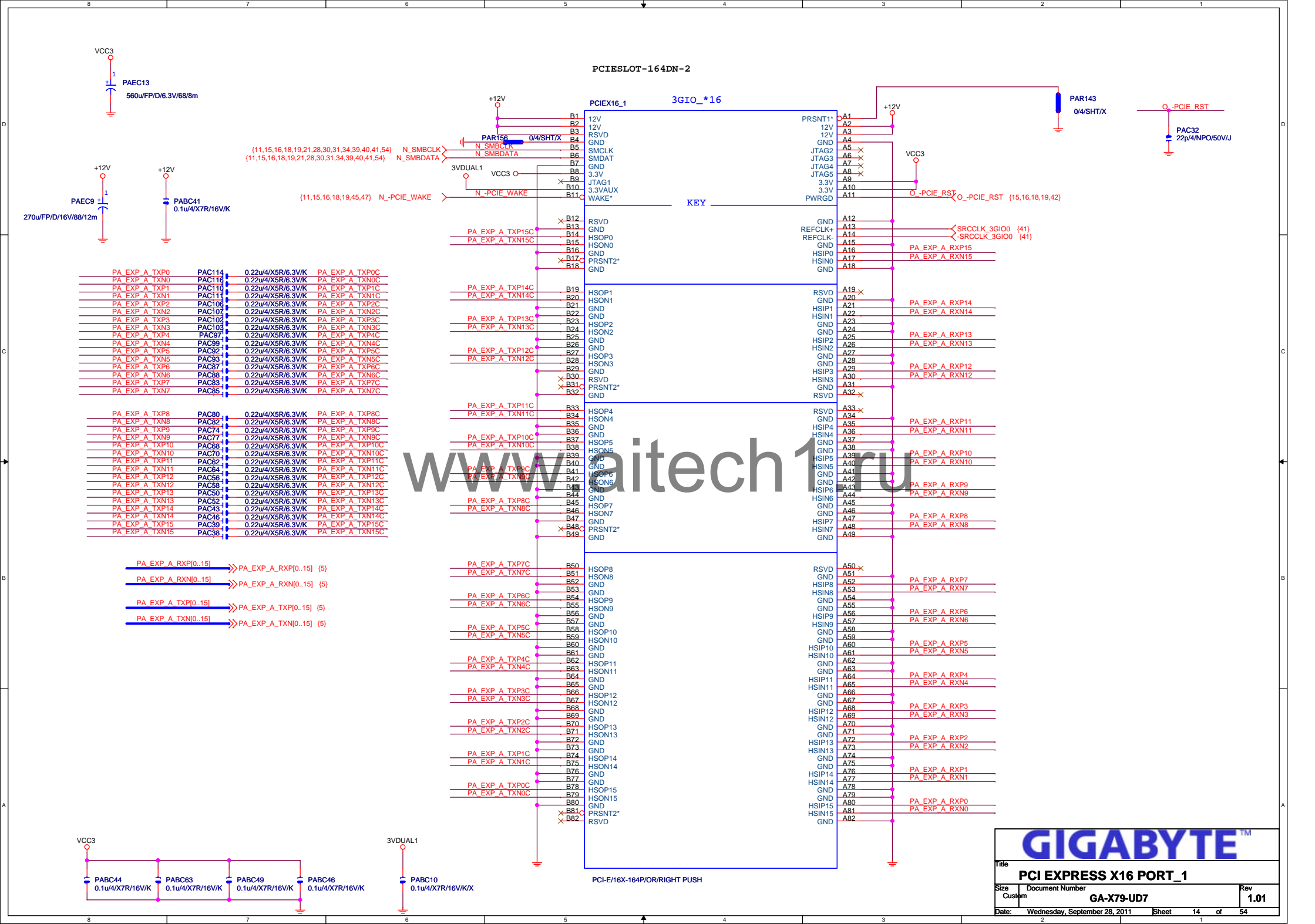


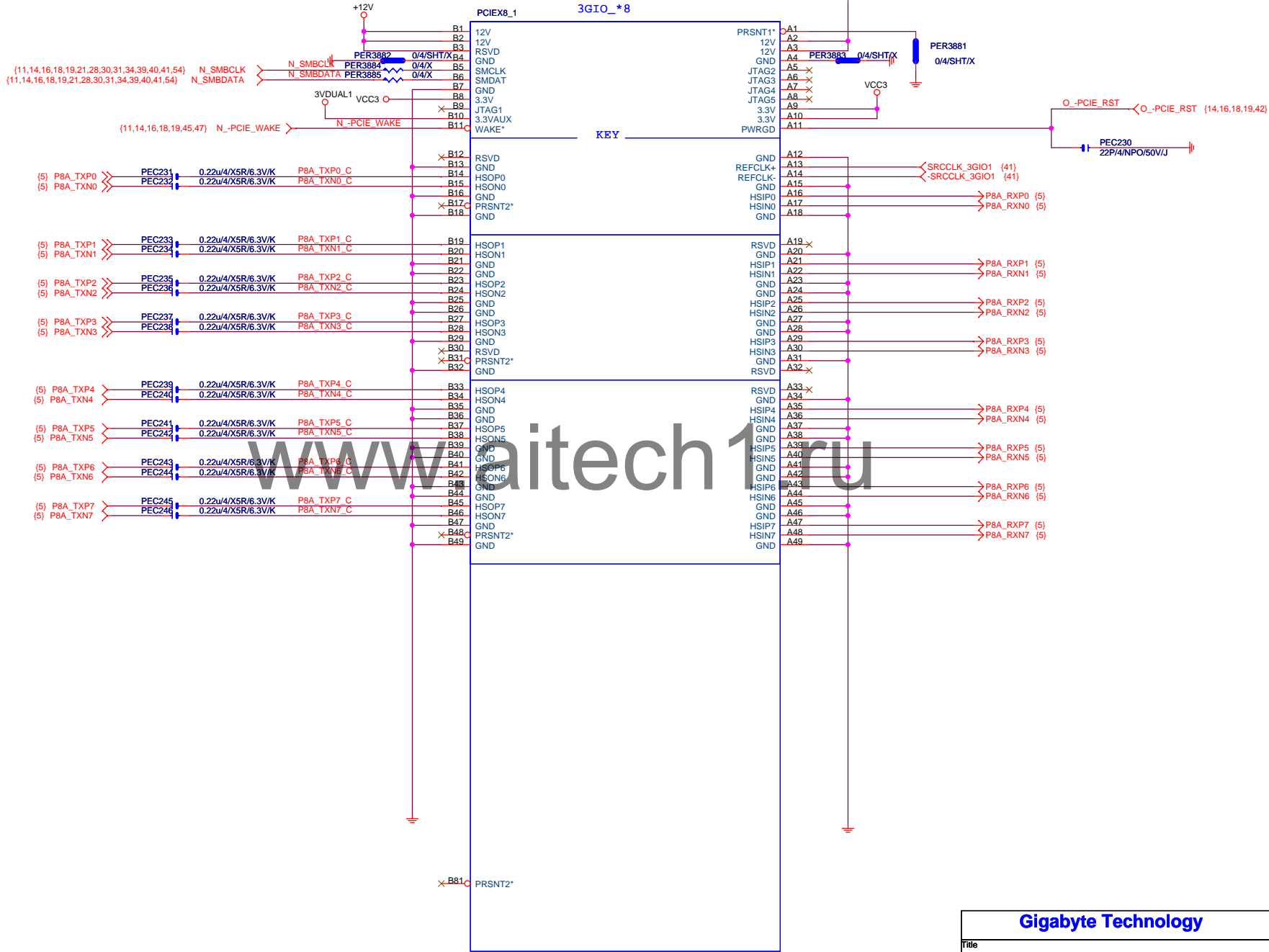
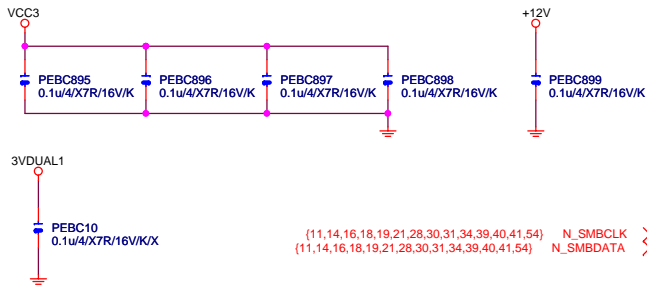


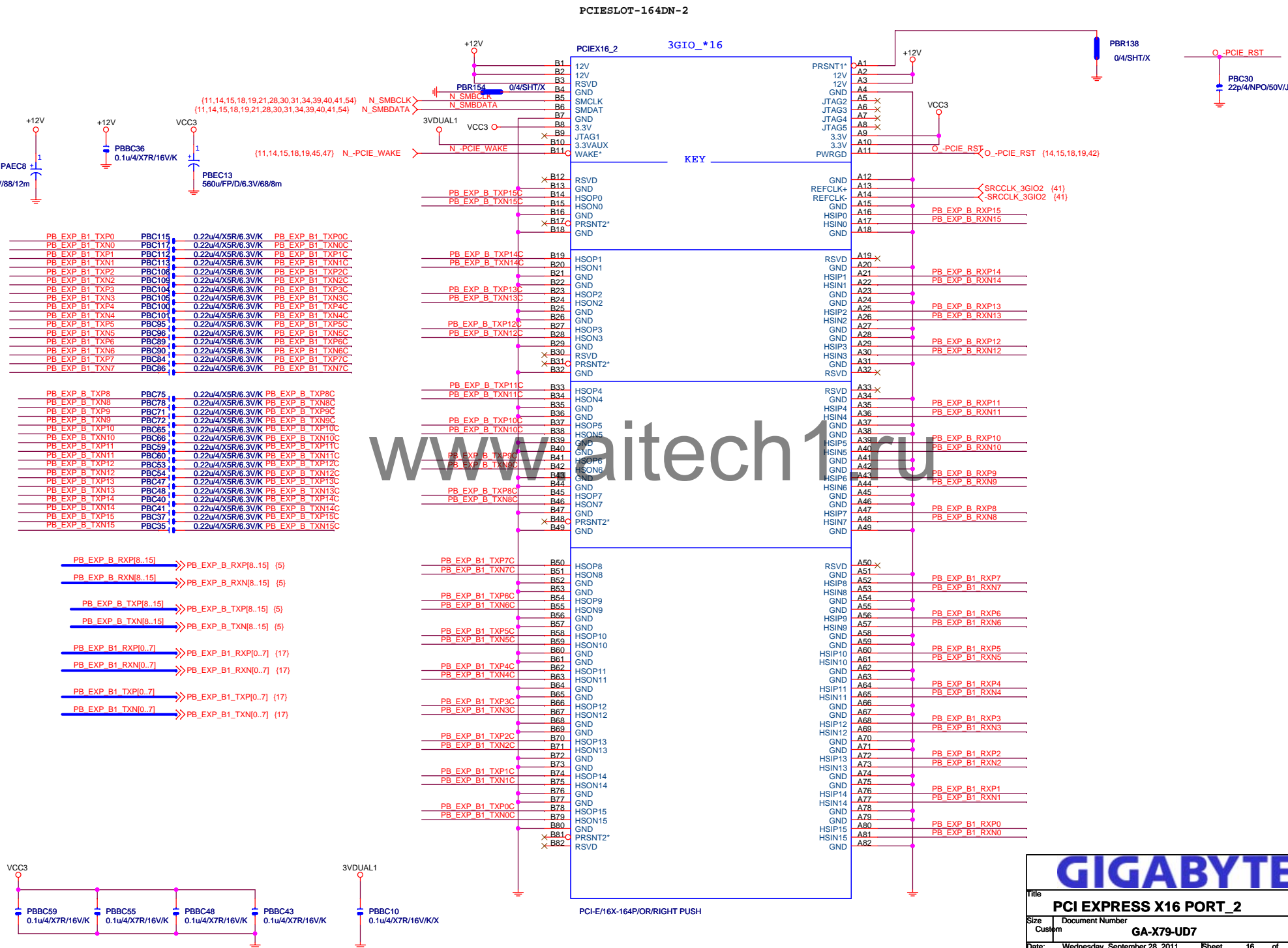


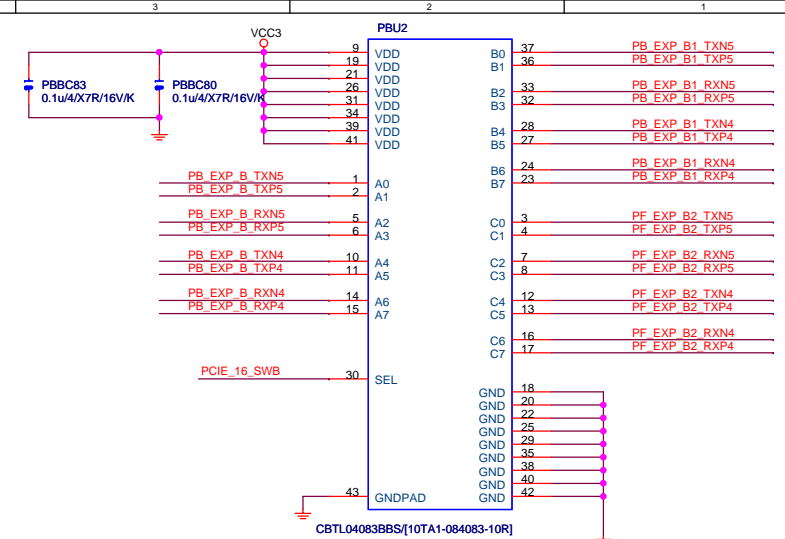












**PBU1**

VCC3 9  
19  
21  
26  
31  
34  
39  
41

GND 10  
11  
14  
15

PCIE\_16\_SWB 30

SEL 43

GNDPAD

PBBC71 0.1u4/X7R/16V/K

PBBC78 0.1u4/X7R/16V/K

PB EXP B TXN7 1  
PB EXP B TXP7 2  
PB EXP B RXN7 5  
PB EXP B RXP7 6  
PB EXP B TXN6 10  
PB EXP B TXP6 11  
PB EXP B RXN6 14  
PB EXP B RXP6 15

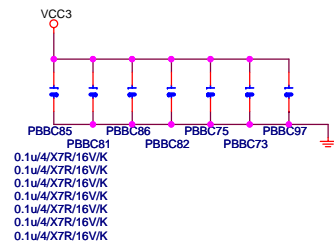
A0  
A1  
A2  
A3  
A4  
A5  
A6  
A7

B0 37  
B1 36  
B2 33  
B3 32  
B4 28  
B5 27  
B6 24  
B7 23  
C0 3  
C1 4  
C2 7  
C3 8  
C4 12  
C5 13  
C6 16  
C7 17

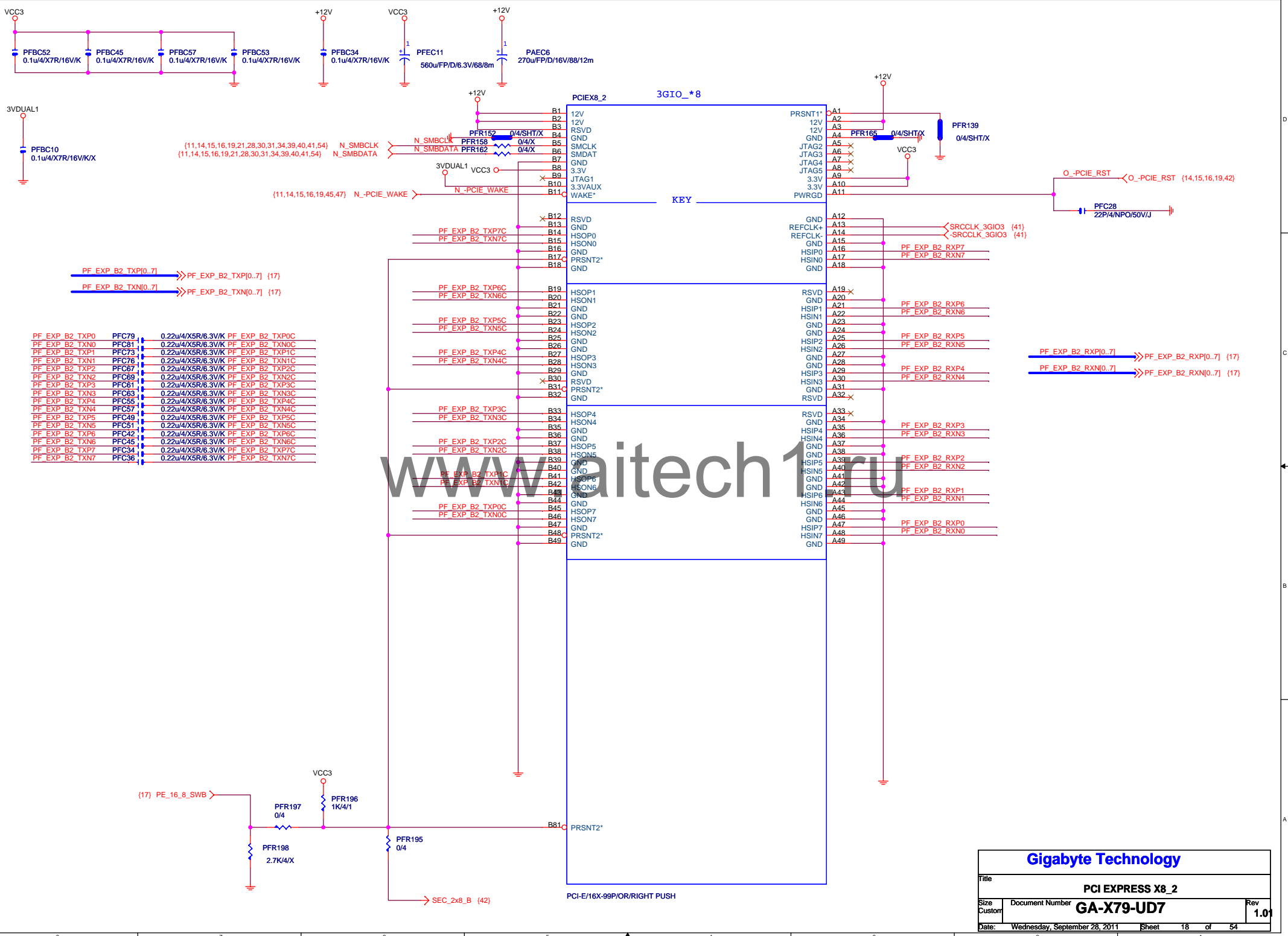
GND 18  
GND 20  
GND 22  
GND 25  
GND 29  
GND 35  
GND 38  
GND 40  
GND 42

PB EXP B1 TXN7  
PB EXP B1 TXP7  
PB EXP B1 RXN7  
PB EXP B1 RXP7  
PB EXP B1 TXN6  
PB EXP B1 TXP6  
PB EXP B1 RXN6  
PB EXP B1 RXP6  
PF EXP B2 TXN7  
PF EXP B2 TXP7  
PF EXP B2 RXN7  
PF EXP B2 RXP7  
PF EXP B2 TXN6  
PF EXP B2 TXP6  
PF EXP B2 RXN6  
PF EXP B2 RXP6

CBTL04083BBS(10TA1-084083-10R)







(11,14,15,16,19,21,28,30,31,34,39,40,41,54) N\_SMBCLK  
(11,14,15,16,19,21,28,30,31,34,39,40,41,54) N\_SMBDATA

(11,14,15,16,19,45,47) N\_-PCIE\_WAKE

PF\_EXP\_B2\_TXP[0..7] >> PF\_EXP\_B2\_TXP[0..7] (17)  
PF\_EXP\_B2\_TXN[0..7] >> PF\_EXP\_B2\_TXN[0..7] (17)

PF_EXP_B2_TXP0	PFC79	0.22u/4/X5R/6.3V/K	PF_EXP_B2_TXP0C
PF_EXP_B2_TXN0	PFC81	0.22u/4/X5R/6.3V/K	PF_EXP_B2_TXN0C
PF_EXP_B2_TXP1	PFC73	0.22u/4/X5R/6.3V/K	PF_EXP_B2_TXP1C
PF_EXP_B2_TXN1	PFC76	0.22u/4/X5R/6.3V/K	PF_EXP_B2_TXN1C
PF_EXP_B2_TXP2	PFC67	0.22u/4/X5R/6.3V/K	PF_EXP_B2_TXP2C
PF_EXP_B2_TXN2	PFC69	0.22u/4/X5R/6.3V/K	PF_EXP_B2_TXN2C
PF_EXP_B2_TXP3	PFC61	0.22u/4/X5R/6.3V/K	PF_EXP_B2_TXP3C
PF_EXP_B2_TXN3	PFC63	0.22u/4/X5R/6.3V/K	PF_EXP_B2_TXN3C
PF_EXP_B2_TXP4	PFC55	0.22u/4/X5R/6.3V/K	PF_EXP_B2_TXP4C
PF_EXP_B2_TXN4	PFC57	0.22u/4/X5R/6.3V/K	PF_EXP_B2_TXN4C
PF_EXP_B2_TXP5	PFC49	0.22u/4/X5R/6.3V/K	PF_EXP_B2_TXP5C
PF_EXP_B2_TXN5	PFC51	0.22u/4/X5R/6.3V/K	PF_EXP_B2_TXN5C
PF_EXP_B2_TXP6	PFC42	0.22u/4/X5R/6.3V/K	PF_EXP_B2_TXP6C
PF_EXP_B2_TXN6	PFC45	0.22u/4/X5R/6.3V/K	PF_EXP_B2_TXN6C
PF_EXP_B2_TXP7	PFC34	0.22u/4/X5R/6.3V/K	PF_EXP_B2_TXP7C
PF_EXP_B2_TXN7	PFC36	0.22u/4/X5R/6.3V/K	PF_EXP_B2_TXN7C

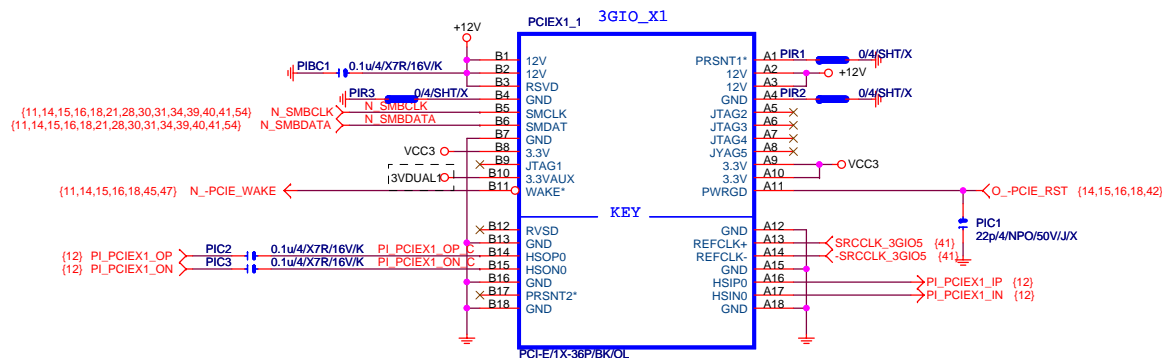
(17) PE\_16\_8\_SWB

SEC\_2x8\_B (42)

<b>Gigabyte Technology</b>			
Title			
PCI EXPRESS X8_2			
Size	Document Number		Rev
Custom	GA-X79-UD7		1.01
Date:	Wednesday, September 28, 2011	Sheet 18 of 54	

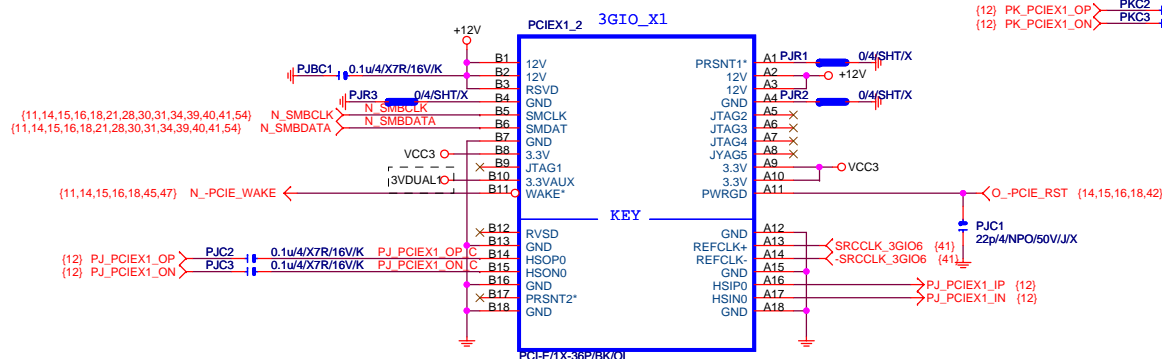


PCIE\*1



PCIE\*1

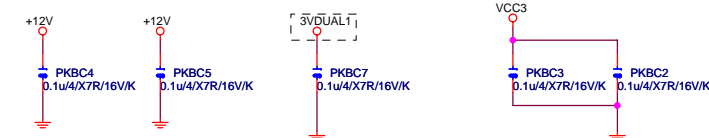
PCIE\*1



PCIE\*1 3GIO\_X1

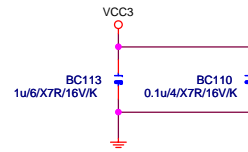
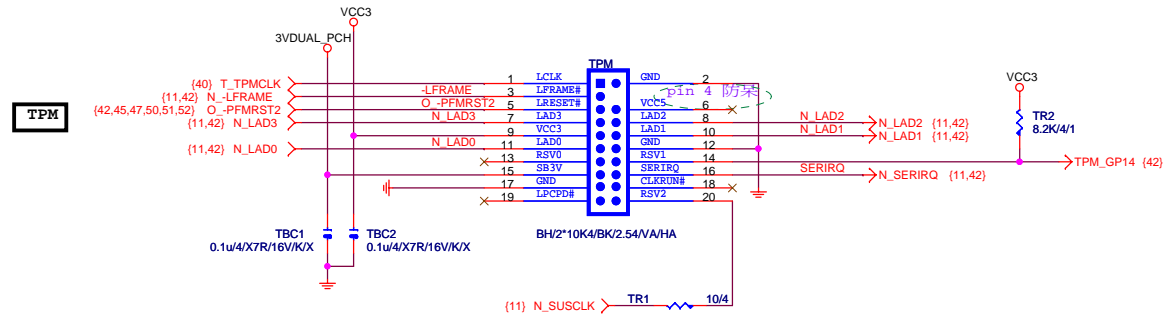
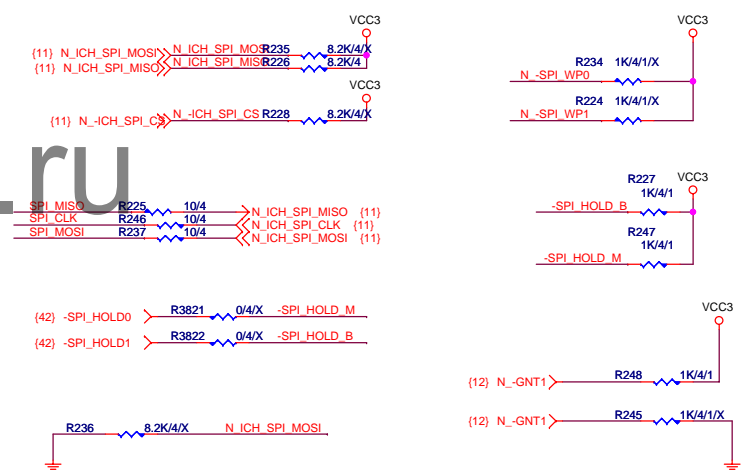
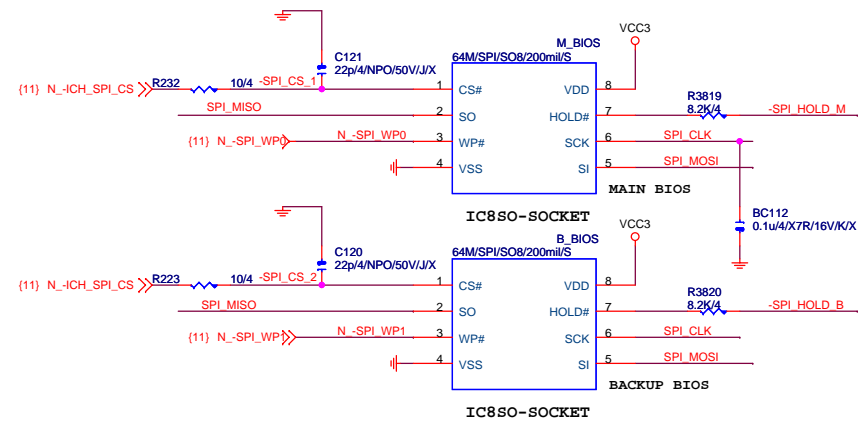
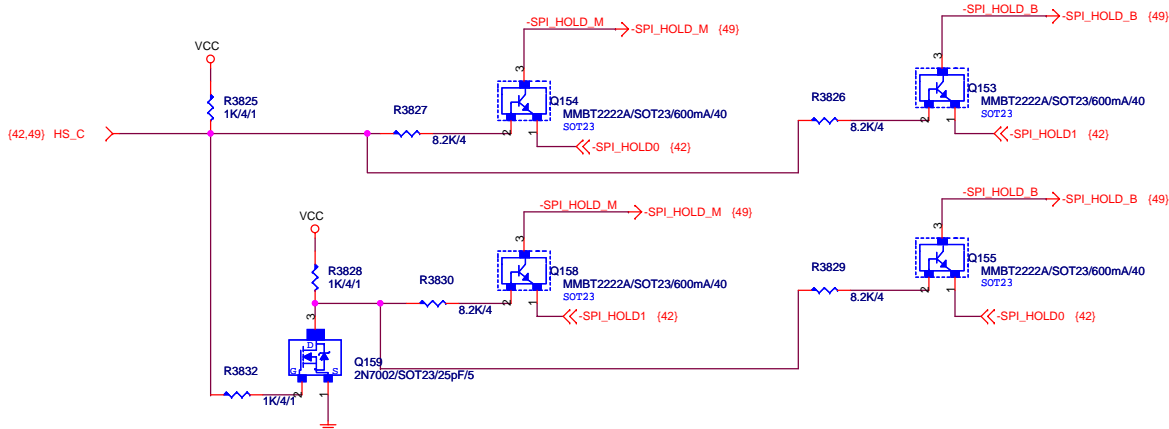


PCIE\*1 3GIO\_X1



**GIGABYTE™**

Title			
PCI EXPRESS X 1			
Size	Document Number	Rev	
Custom	GA-X79-UD7	1.01	
Date:	Wednesday, September 28, 2011	Sheet	19 of 54



BOOT DEVICE	GNT1	GP19
LPC	0	0
PCI	1	0
SPI	1	1

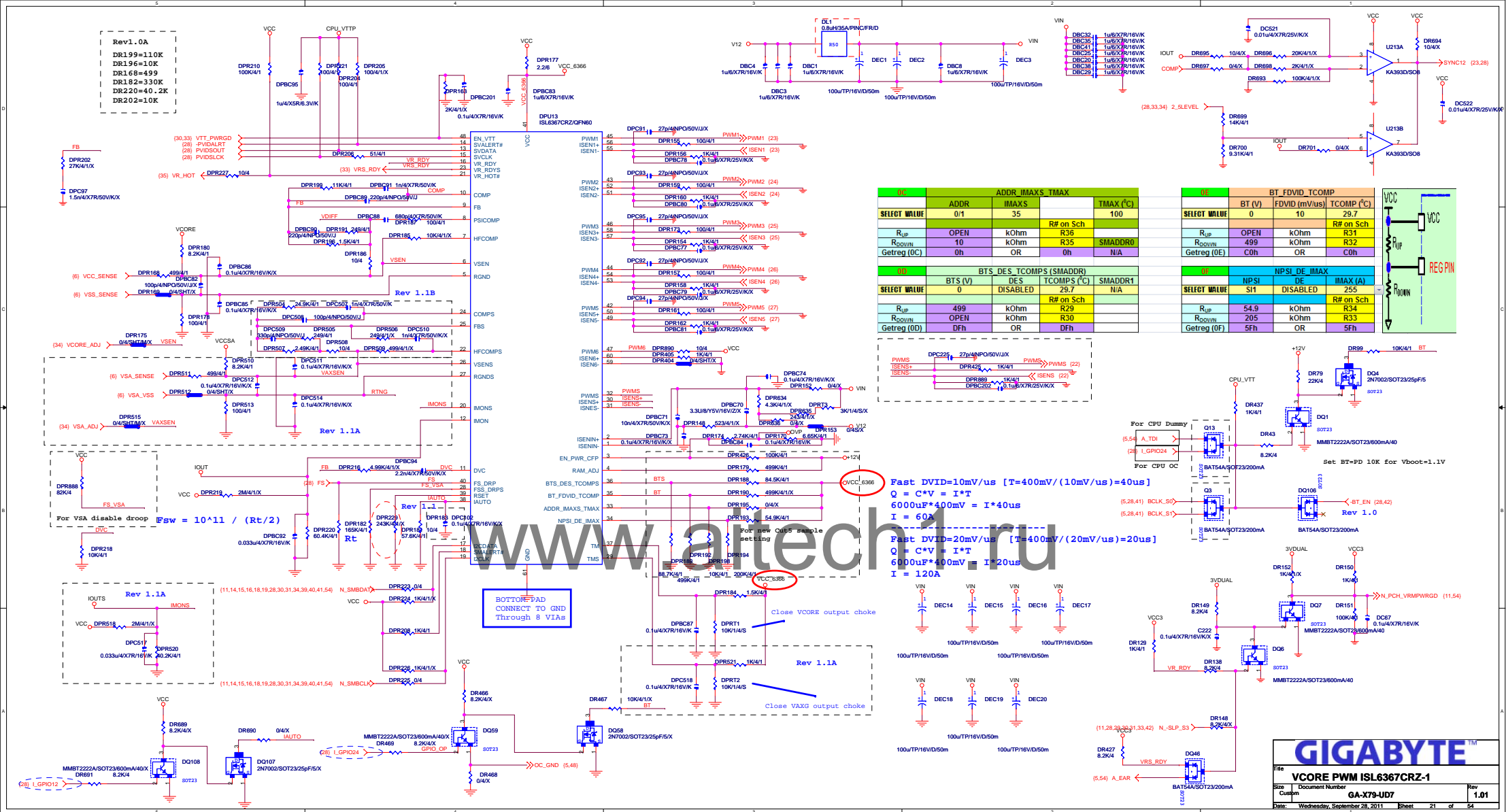
1 means PU  
0 means PD 1K

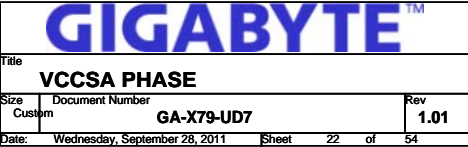
**GIGABYTE™**

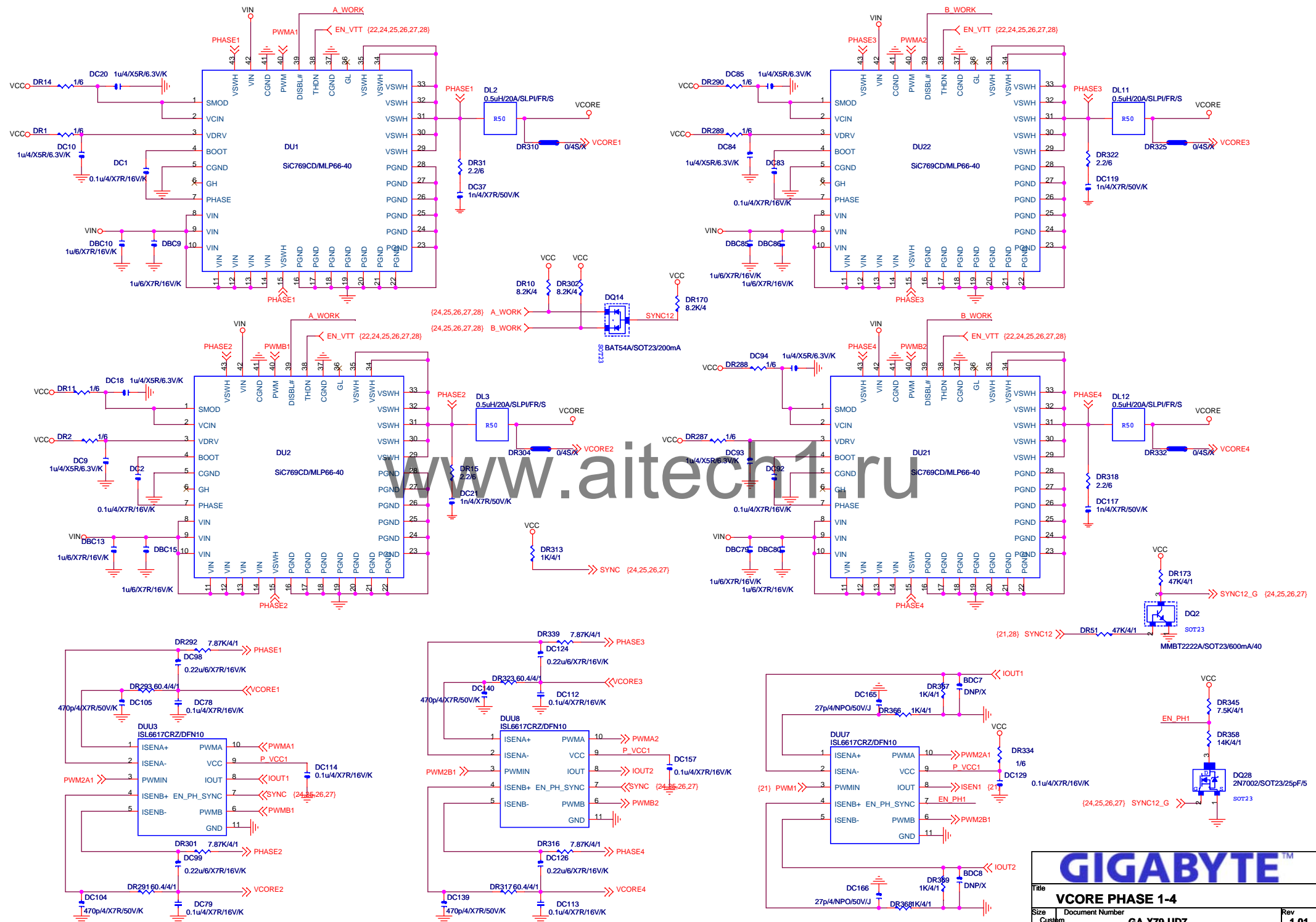
Title: **PCIEX1,DUAL BIOS**

Size: Custom Document Number: **GA-X79-UD7** Rev: **1.01**

Date: Wednesday, September 28, 2011 Sheet: 20 of 54



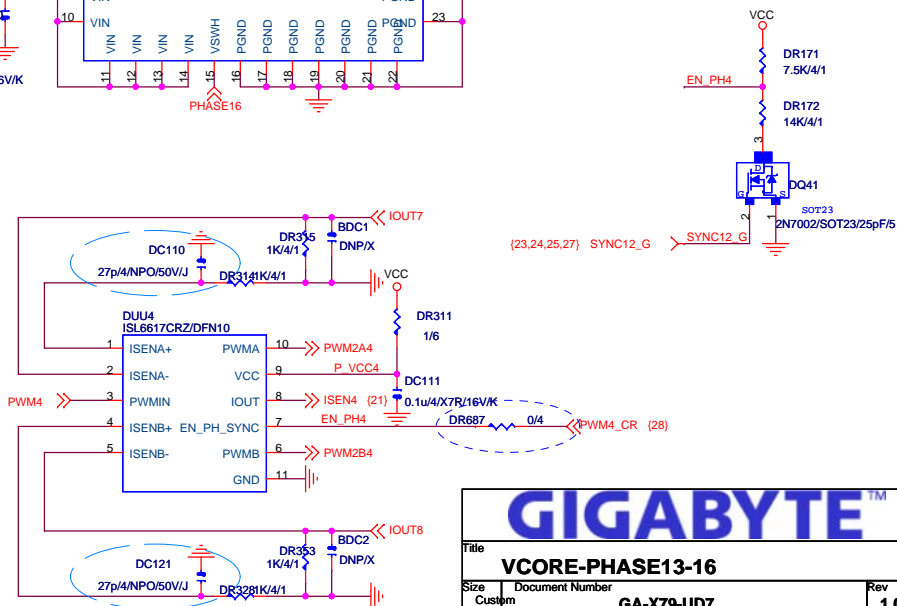
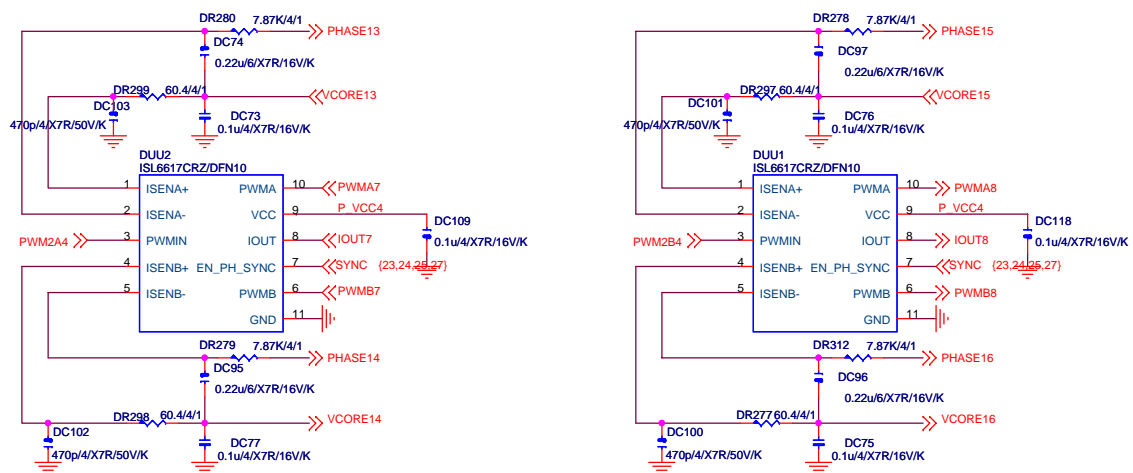
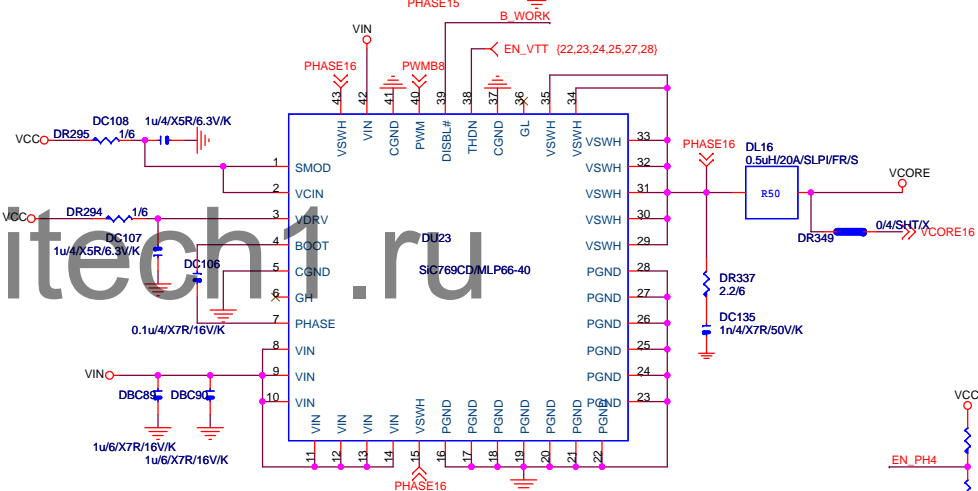
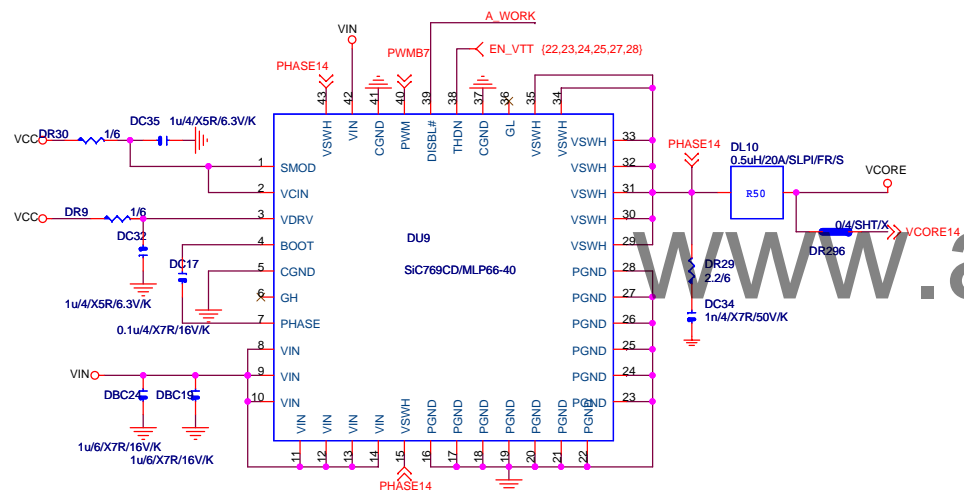
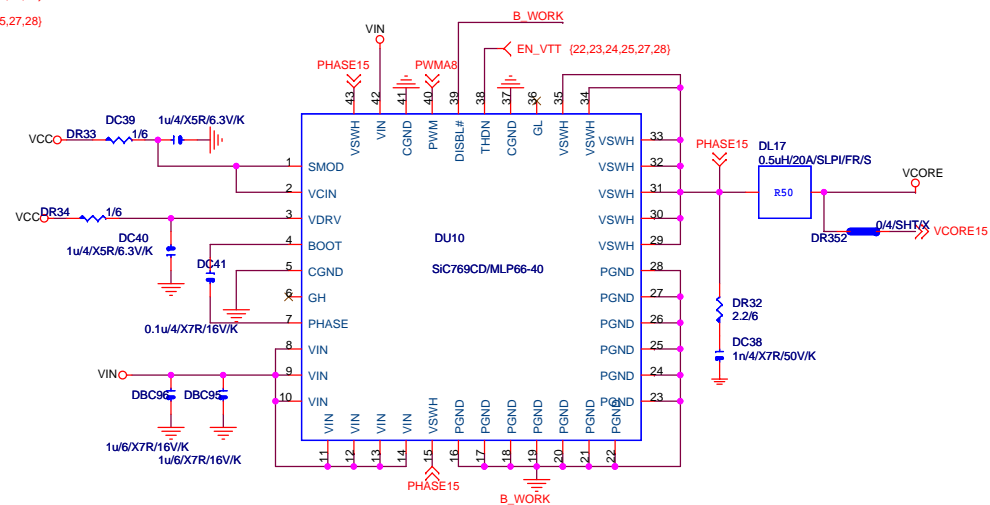
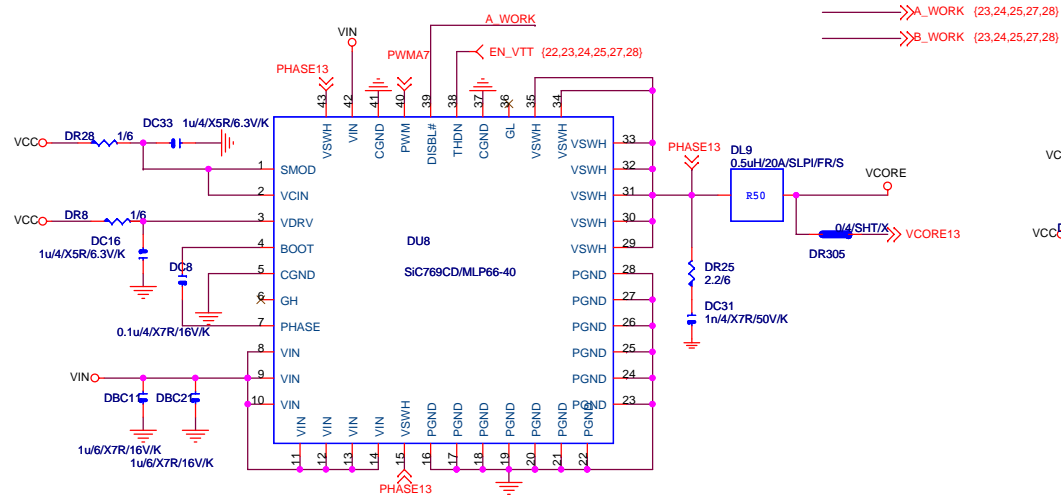






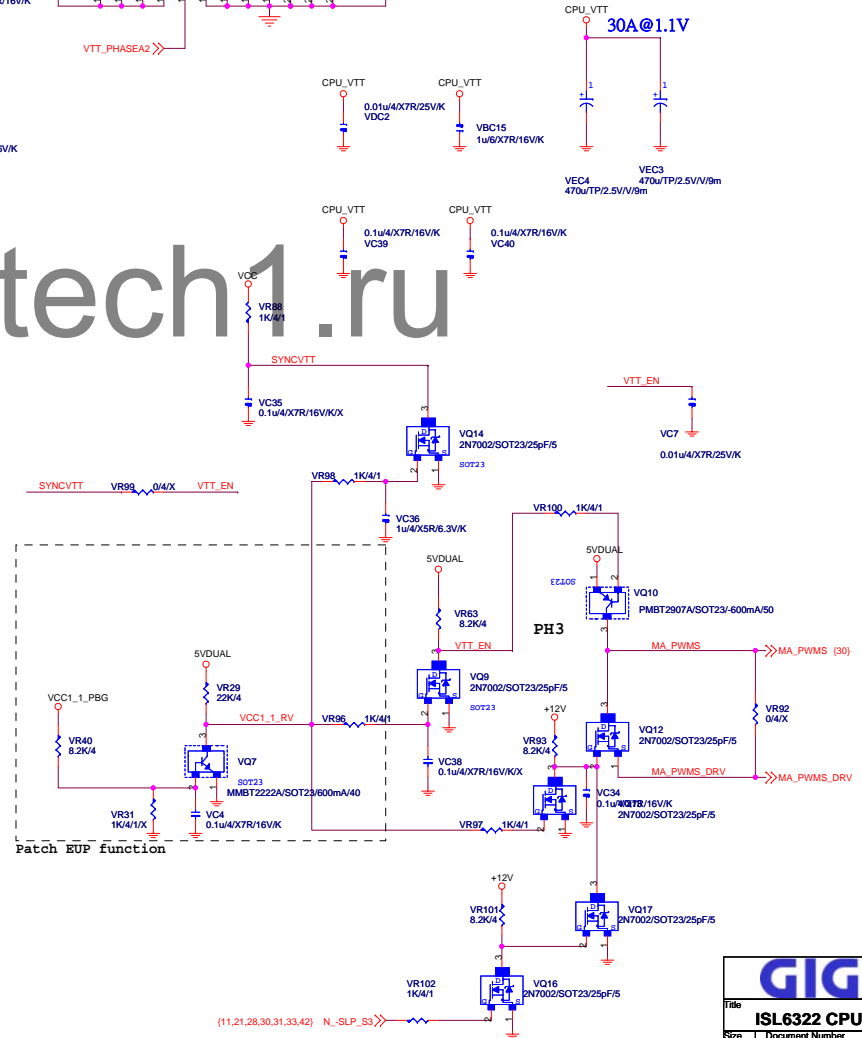
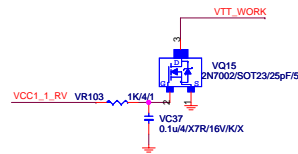
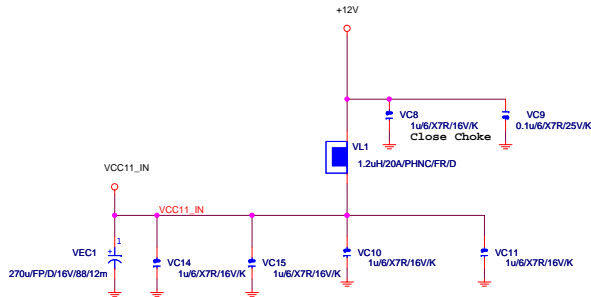
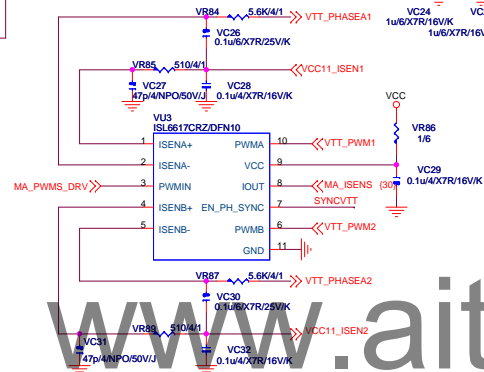
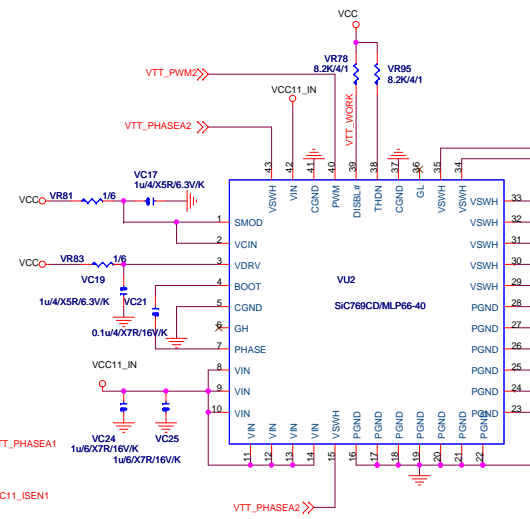
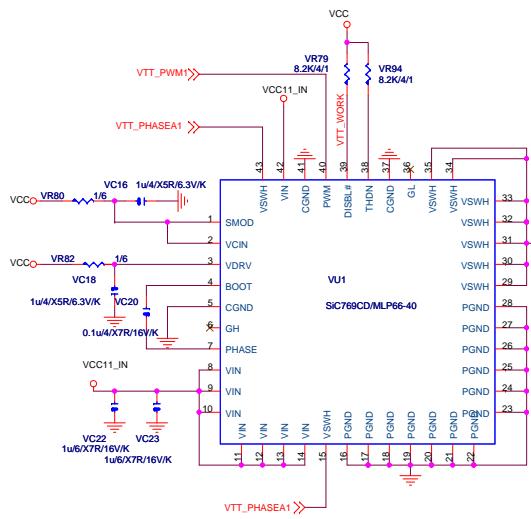


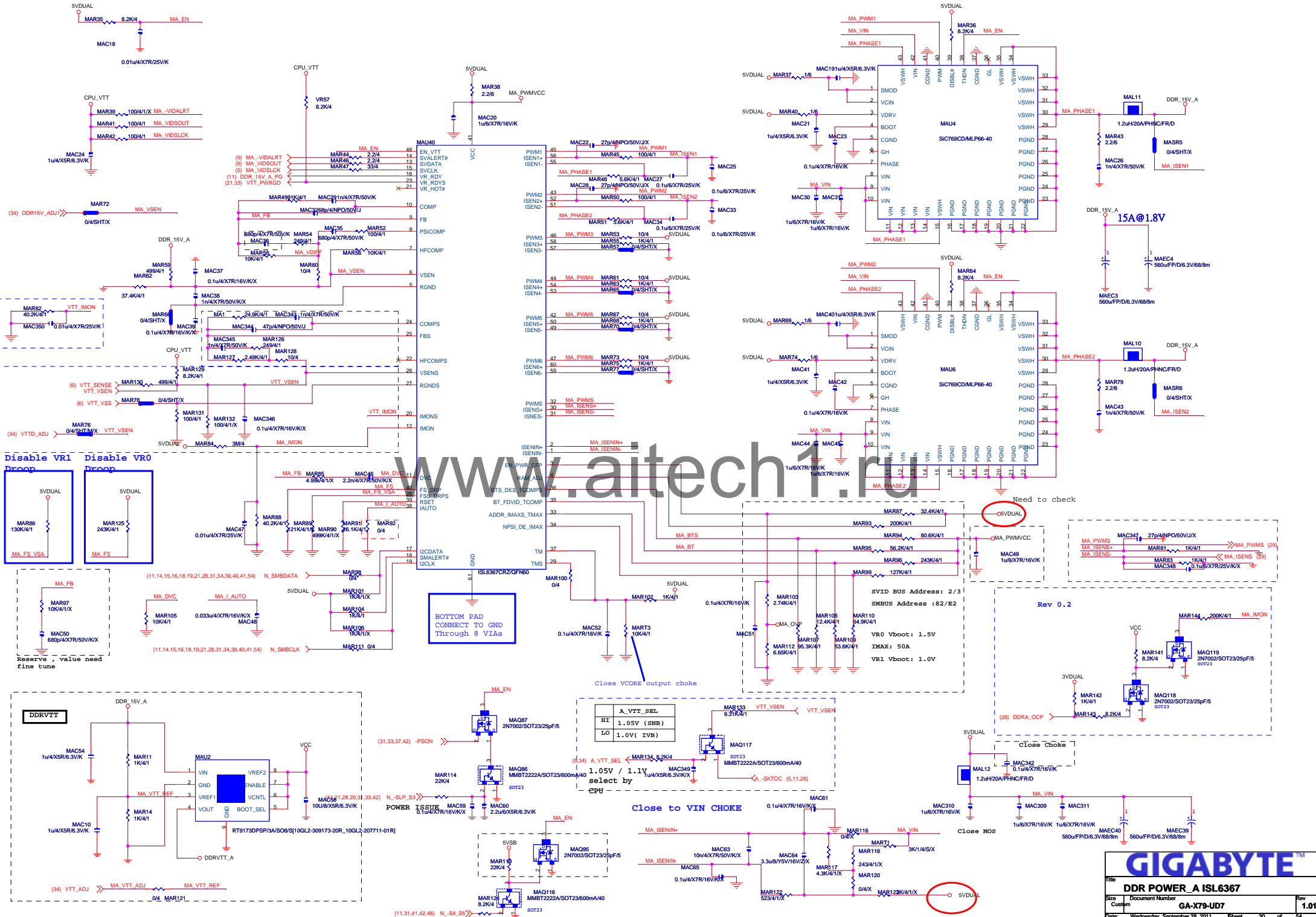


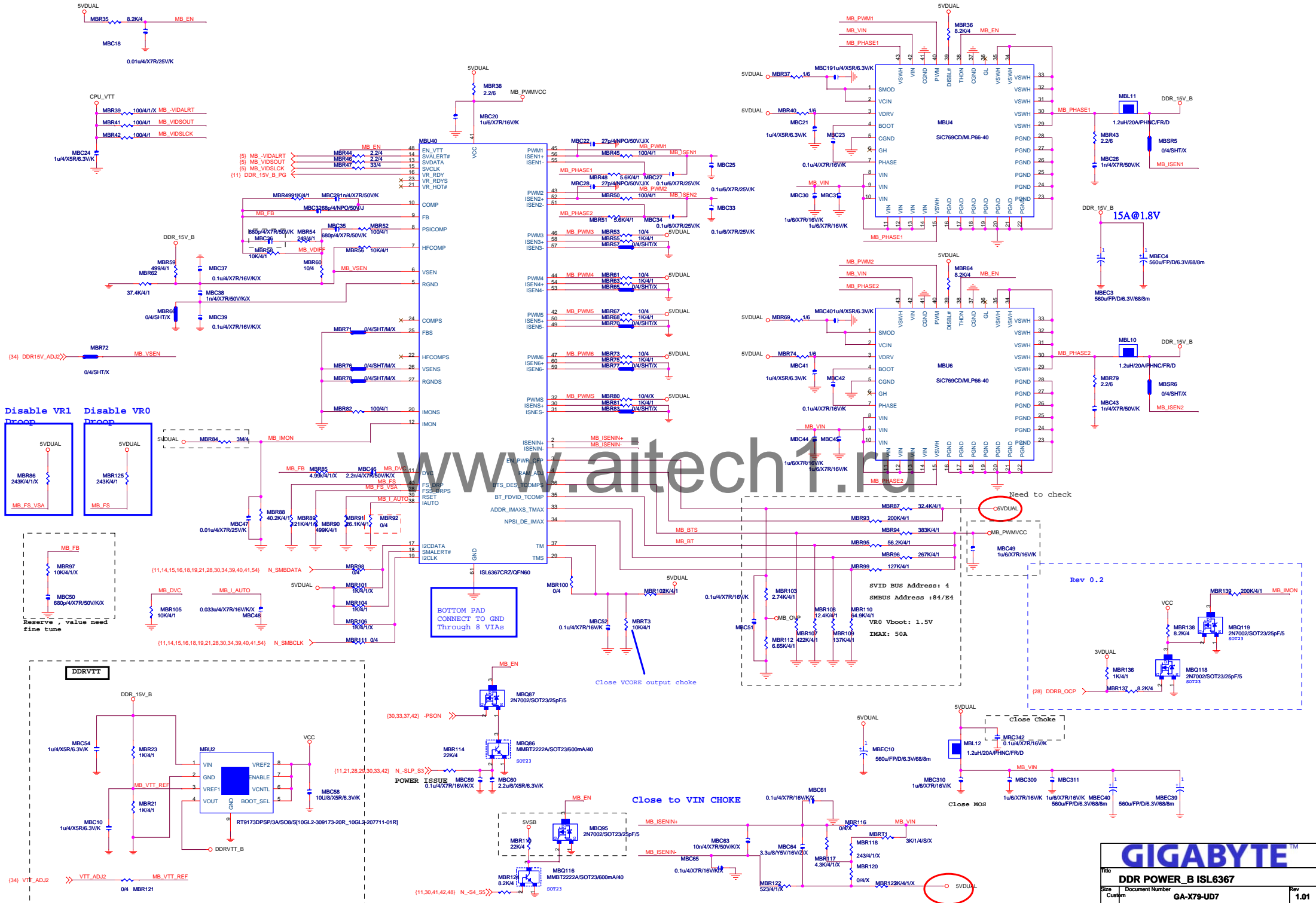




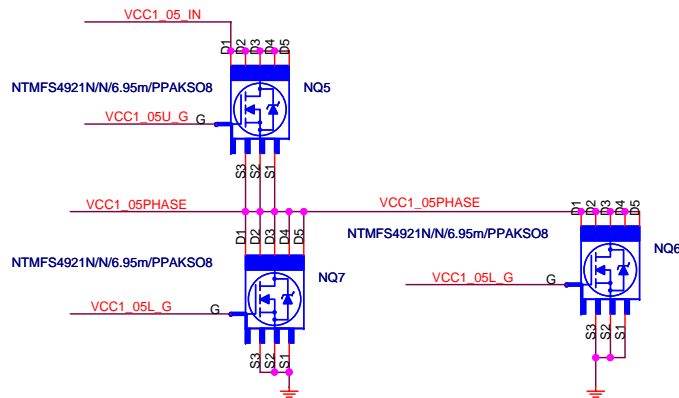
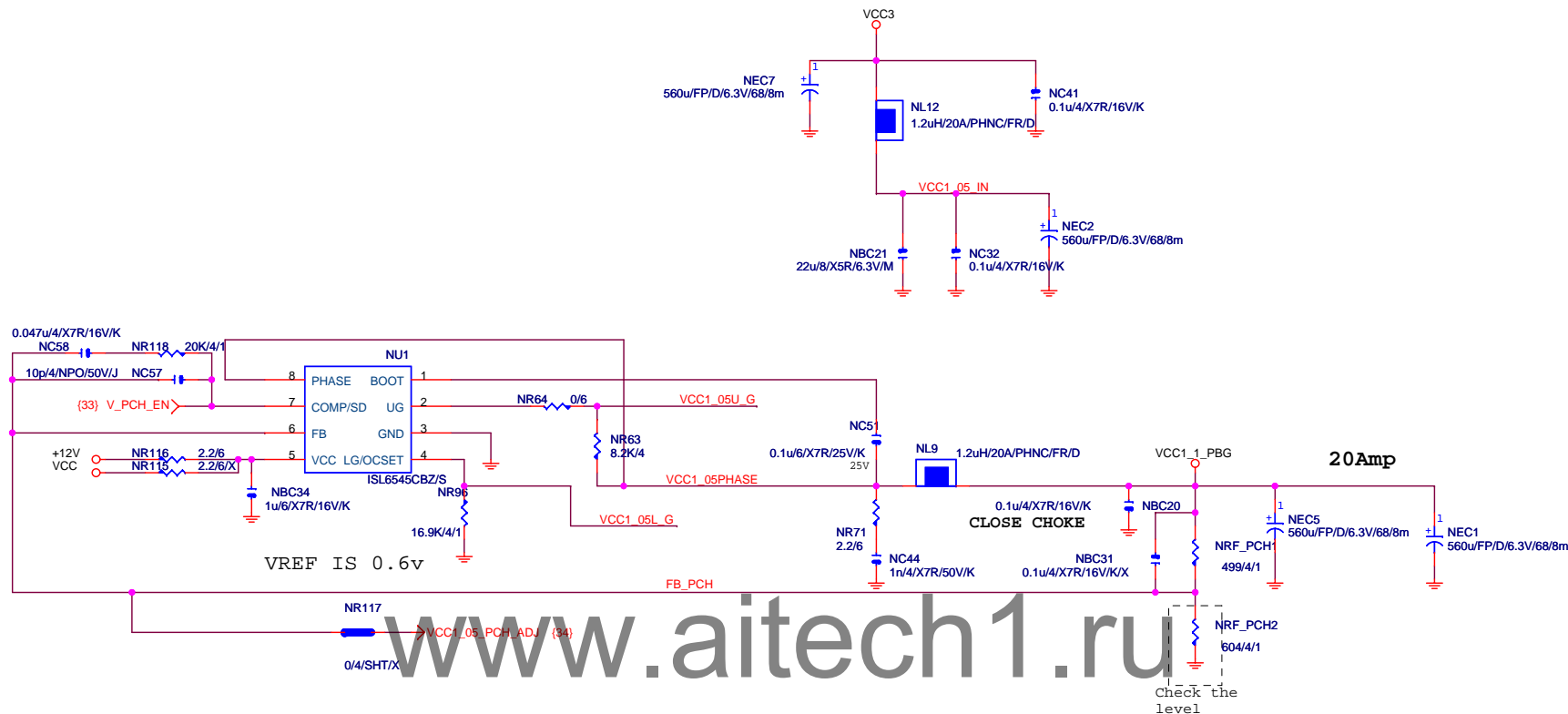








PBG\_1.1V



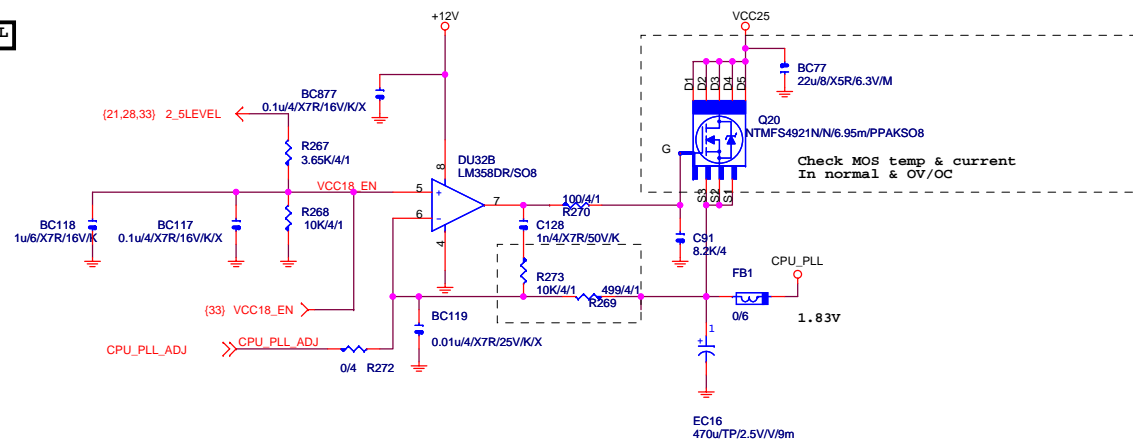
**GIGABYTE™**

Title		
PCH CORE		
Size	Document Number	Rev
B	GA-X79-UD7	1.01
Date:	Wednesday, September 28, 2011	Sheet 32 of 54

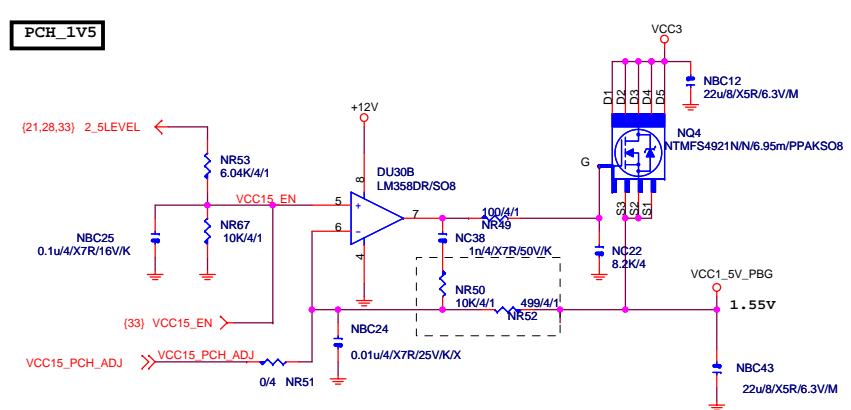




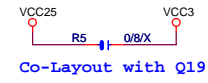
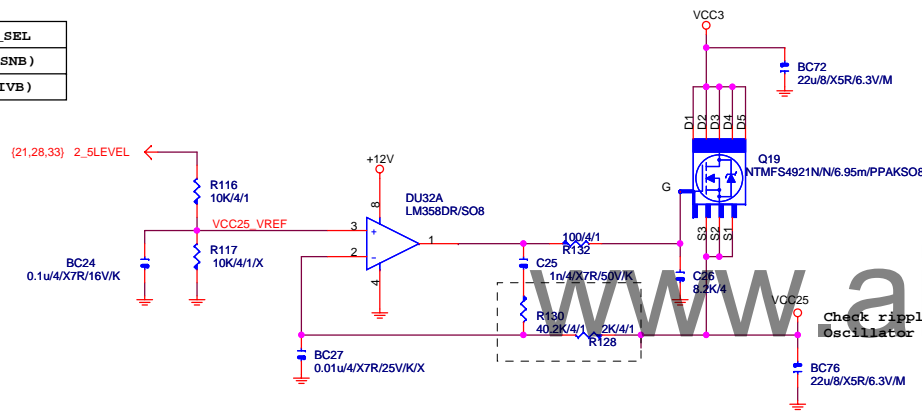
## CPU\_PLL



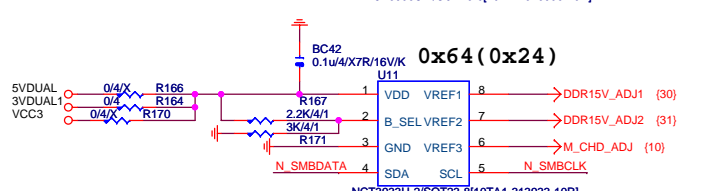
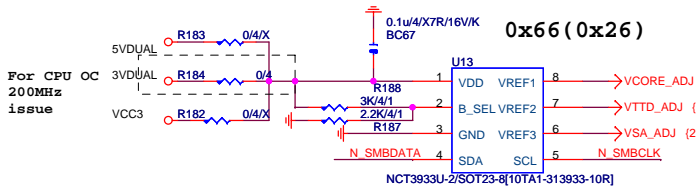
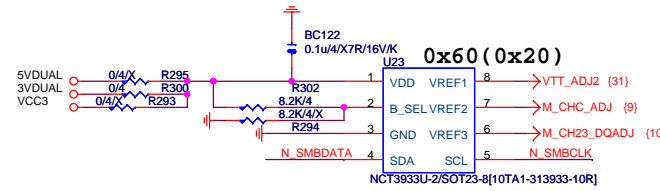
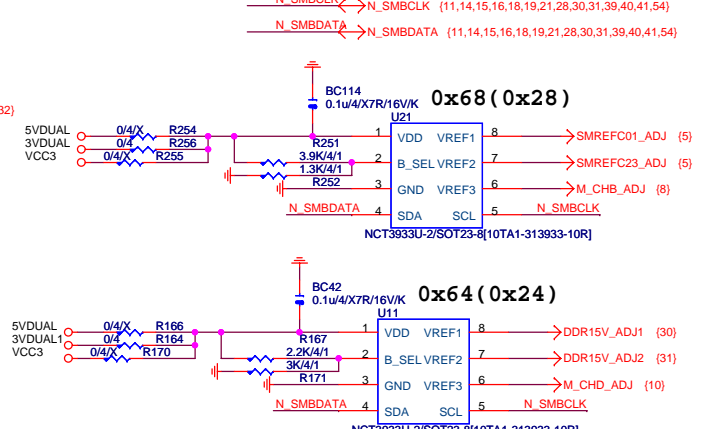
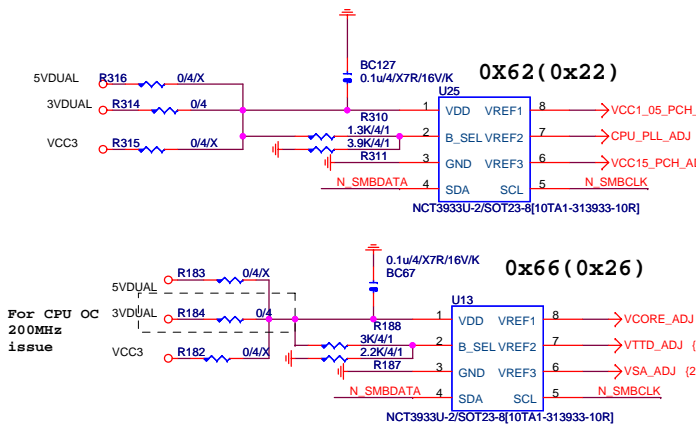
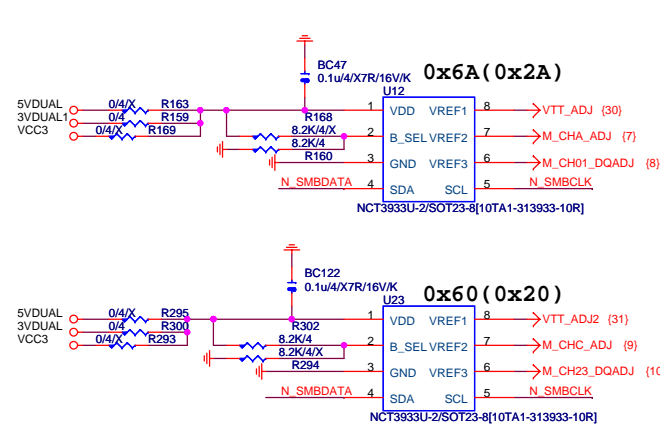
## PCH\_1V5



	A_VTT_SEL
HI	1.8V (SNB)
LO	1.7V (IVB)



	VTT_LEVEL
HI	1.05V
LO	1.0V



**GIGABYTE™**

**DISCRETE POWER II**

File: GA-X79-UD7

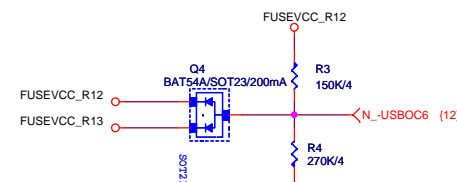
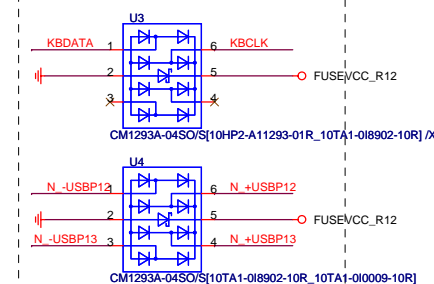
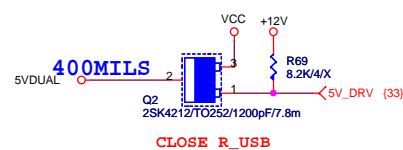
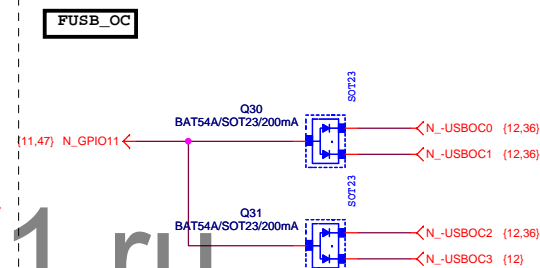
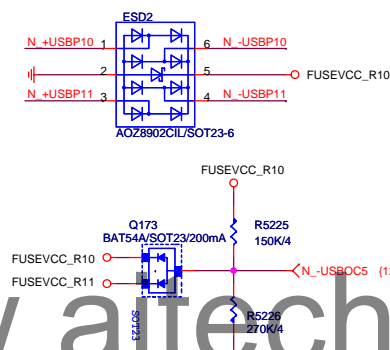
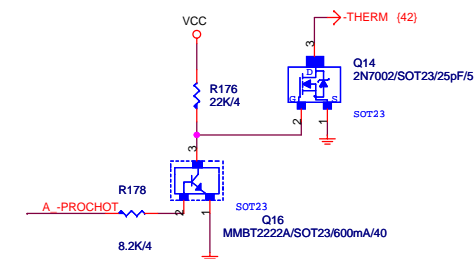
Size: Custom

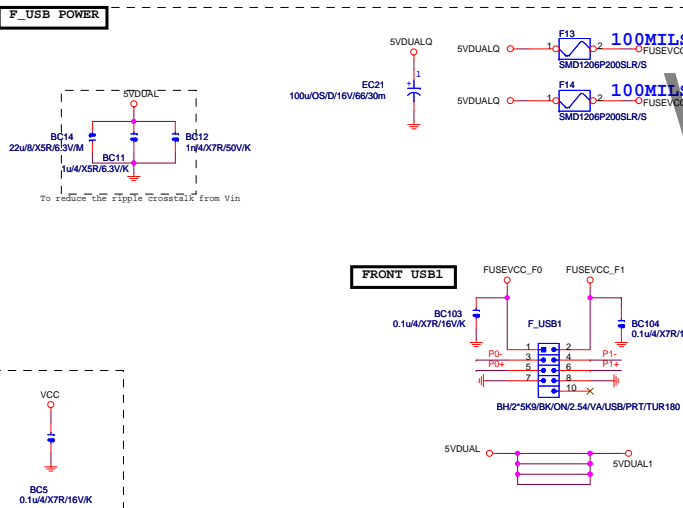
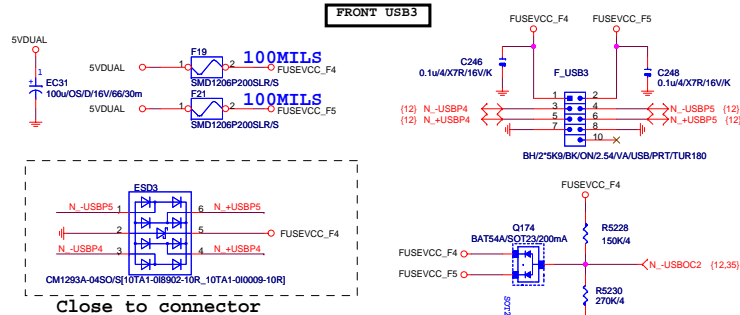
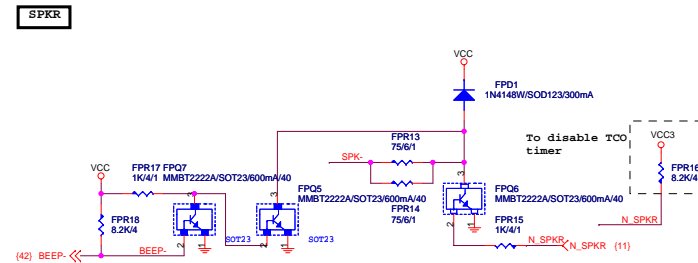
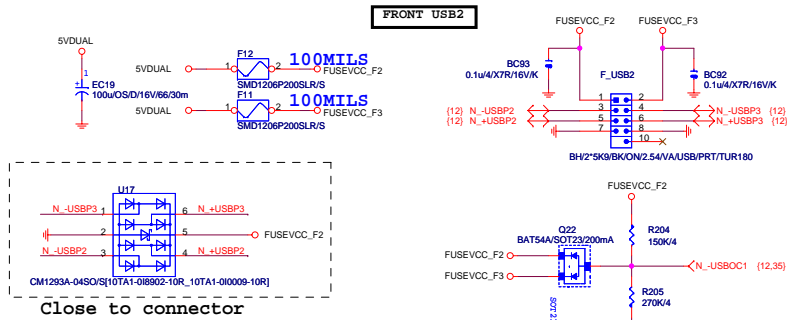
Document Number: GA-X79-UD7

Rev: 1.01

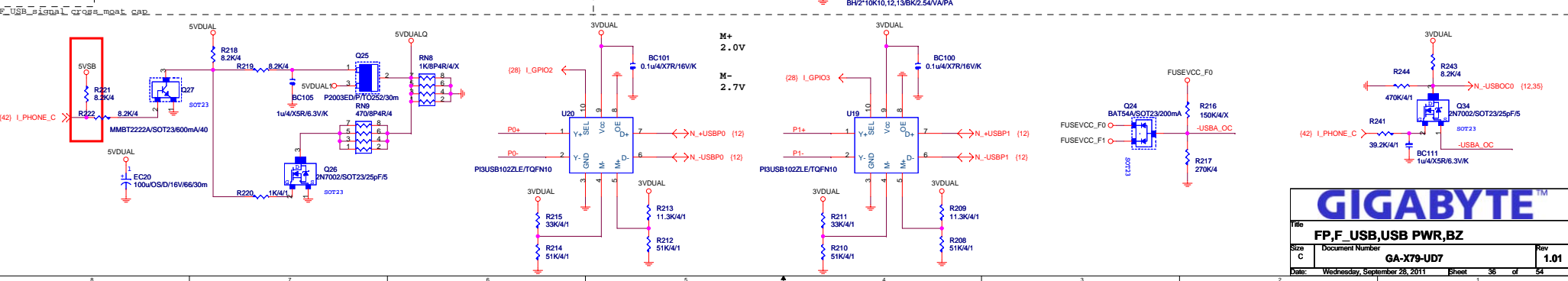
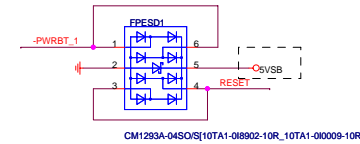
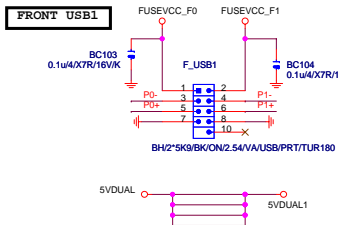
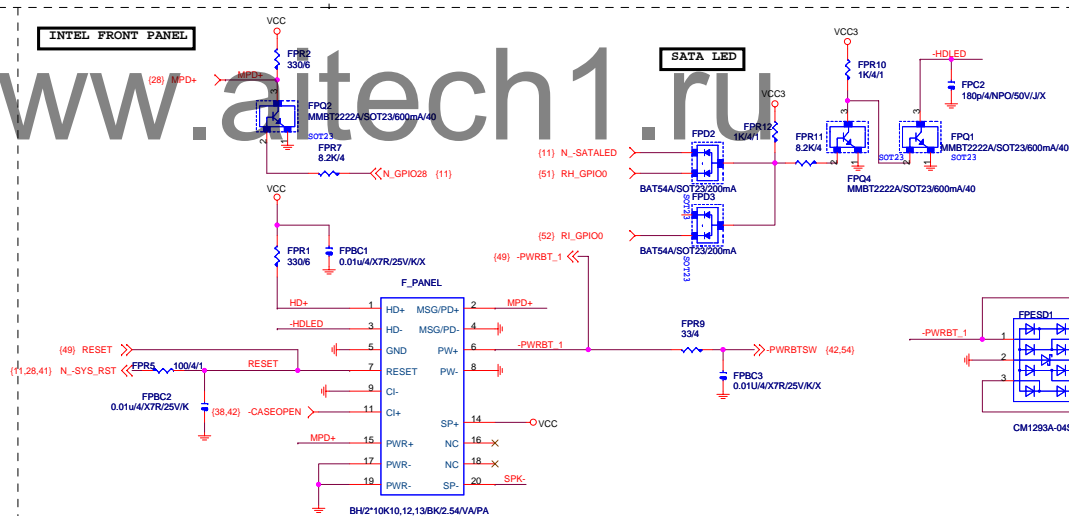
Date: Wednesday, September 28, 2011

Sheet: 34 of 54

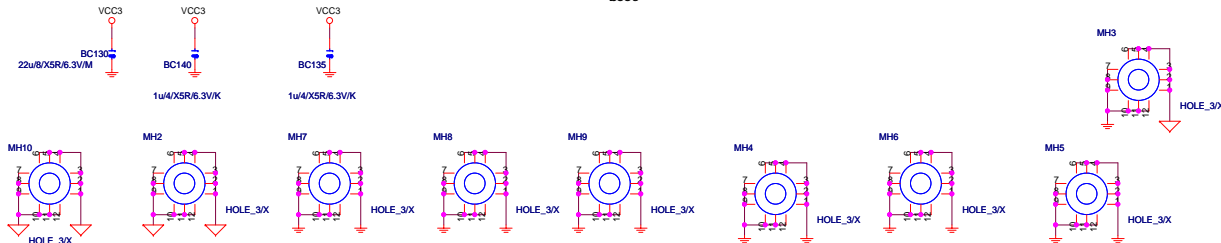
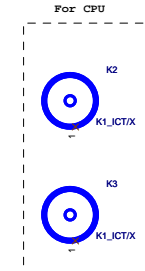
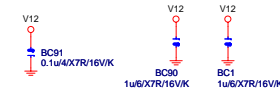




# INTEL FRONT PANEL



The schematic diagram illustrates the internal circuitry of the APW/Z12/BK/VA/SN/2SHK/PA66 power supply module. The module is a 5VSB power supply, which provides a 5V standby power source. The circuit includes a transformer (T1) that steps down the input voltage. The secondary winding of the transformer is connected to a bridge rectifier (AD1) and a filter capacitor (C142). The output of the rectifier and filter is connected to a voltage divider (R323, R324) that provides the 5VSB output. The module also provides outputs for 5V, 12V, and -12V. The schematic shows the connection of the module to the system power supply (VCC) and ground (GND). The module is connected to the system power supply (VCC) and ground (GND) through a series of resistors and capacitors. The module is also connected to the system power supply (VCC) and ground (GND) through a series of resistors and capacitors. The module is connected to the system power supply (VCC) and ground (GND) through a series of resistors and capacitors.



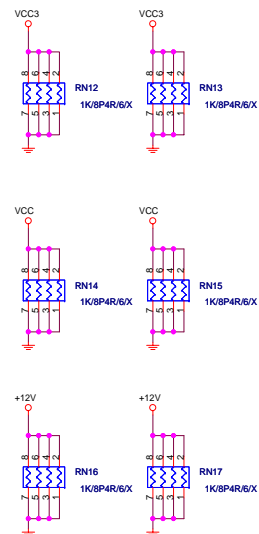
The image shows two pin connection diagrams for ATX headers. The left diagram is for ATX4P1, which has 15 pins. The right diagram is for ATX4P4, which has 14 pins. Both diagrams show the connection of power, ground, and control signals to the header pins.

**ATX4P1 Pin Connections:**

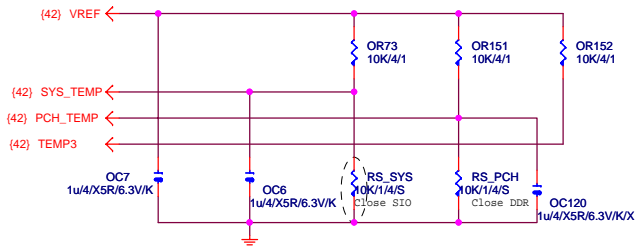
- Pin 15: +12V
- Pin 14: +12V
- Pin 13: +12V
- Pin 12: +12V
- Pin 11: GND
- Pin 10: GND
- Pin 9: VCC
- Pin 8: VCC
- Pin 7: VCC
- Pin 6: GND
- Pin 5: GND
- Pin 4: VCC
- Pin 3: VCC
- Pin 2: VCC
- Pin 1: VCC

**ATX4P4 Pin Connections:**

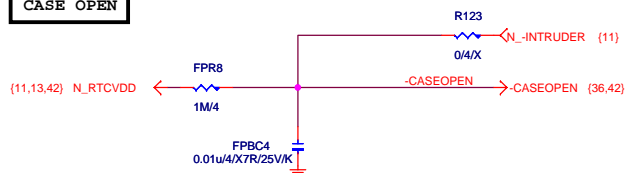
- Pin 15: +12V
- Pin 14: +12V
- Pin 13: +12V
- Pin 12: +12V
- Pin 11: GND
- Pin 10: GND
- Pin 9: VCC
- Pin 8: VCC
- Pin 7: VCC
- Pin 6: GND
- Pin 5: GND
- Pin 4: VCC
- Pin 3: VCC
- Pin 2: VCC
- Pin 1: VCC



# TEMP H/W MONITOR

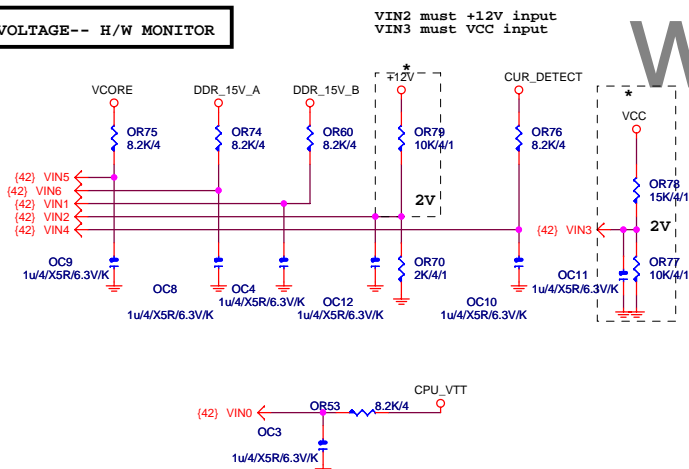


# CASE OPEN

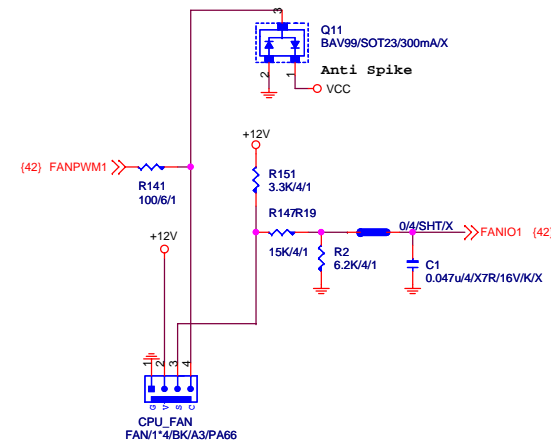


Case Open Circuits

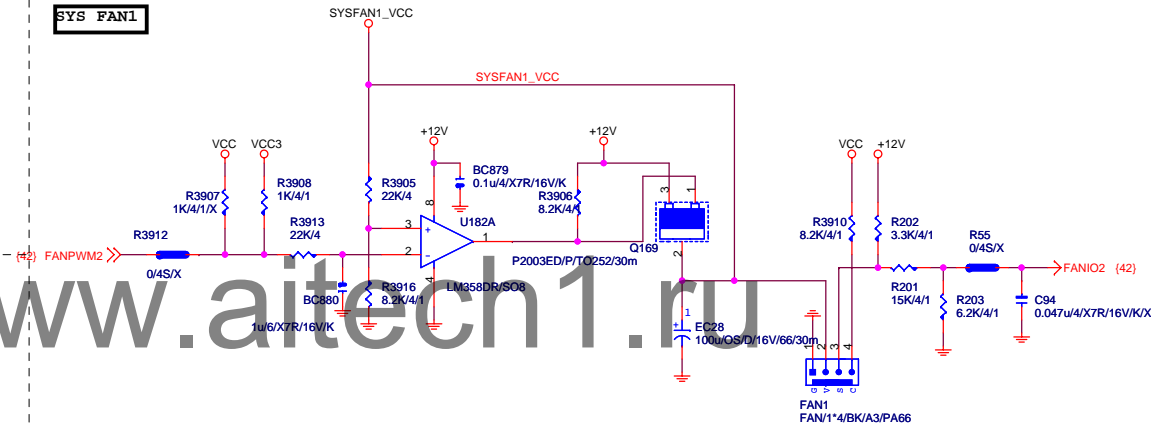
# VOLTAGE-- H/W MONITOR



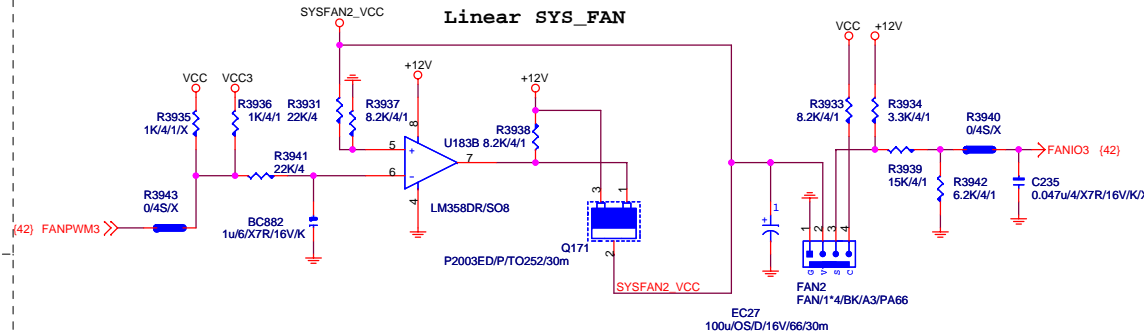
# CPU SMART FAN



# SYS FAN1



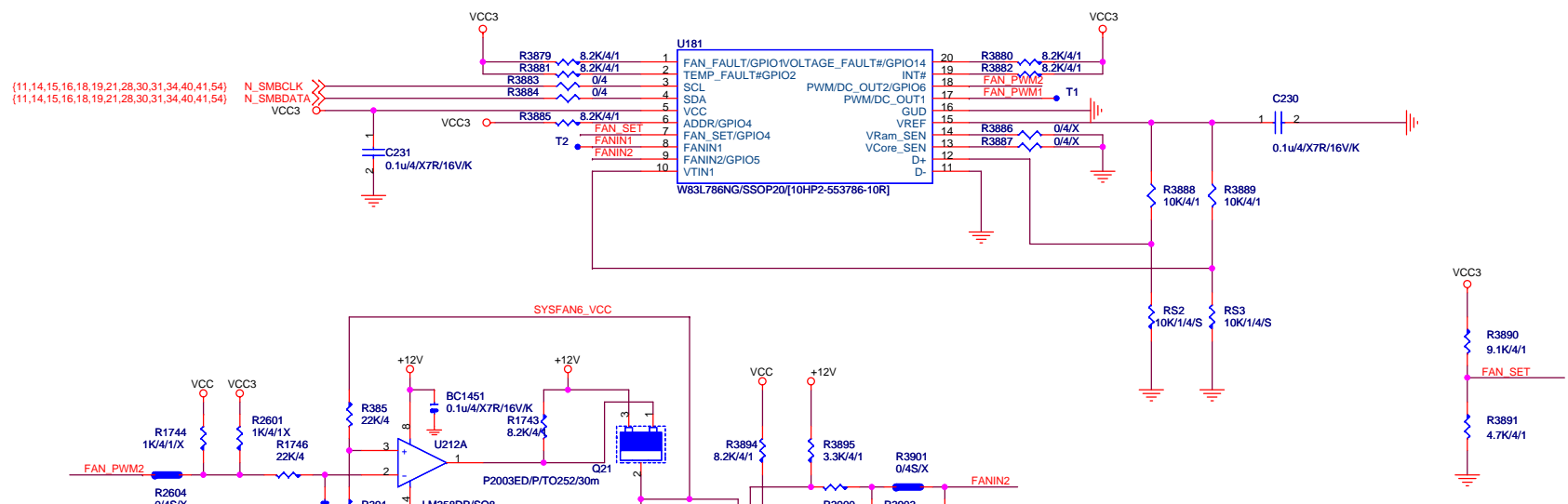
# Linear SYS\_FAN



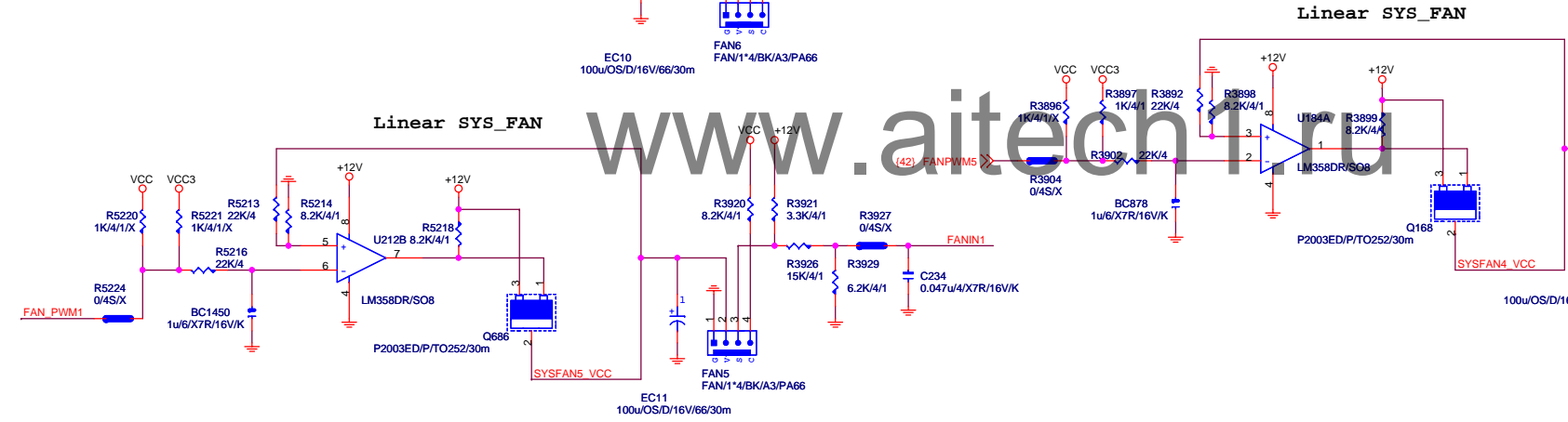
**GIGABYTE**

Title		
HWM,KB/MS, FAN CTRL		
Size	Document Number	Rev
Custom	GA-X79-UD7	1.01
Date:	Wednesday, September 28, 2011	Sheet 38 of 54

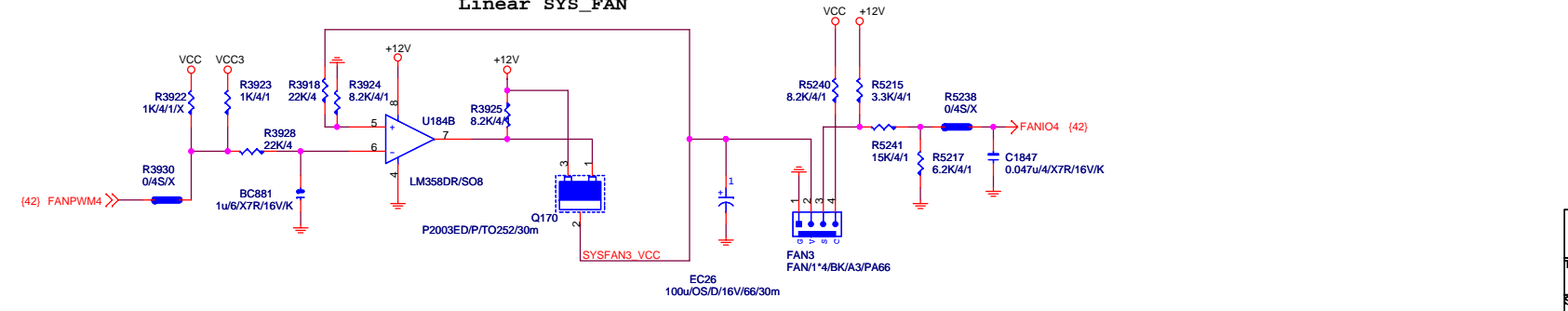




Section	FAN Speed rate	R5185	R5175
3	100%	9.1K	X
2	81%	4.7K	9.1K
1	62%	9.1K	4.7K
0	43%	X	9.1K



**SIO FAN CONTROL**



**GIGABYTE™**

Title: **FAN**

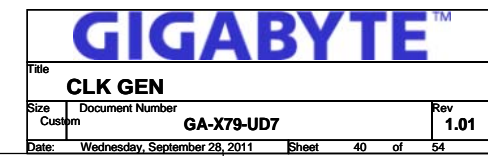
Size: Custom

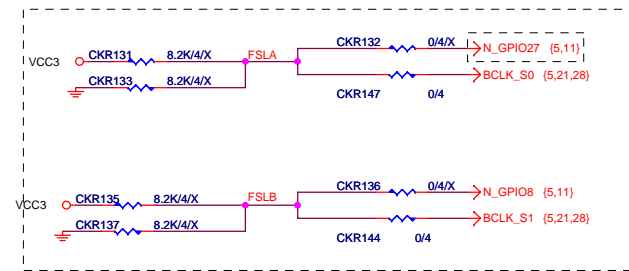
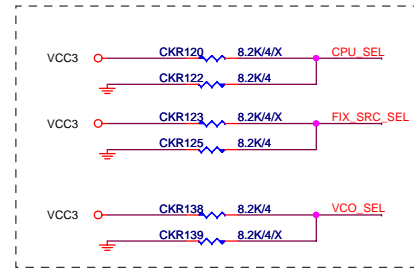
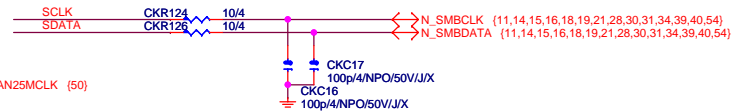
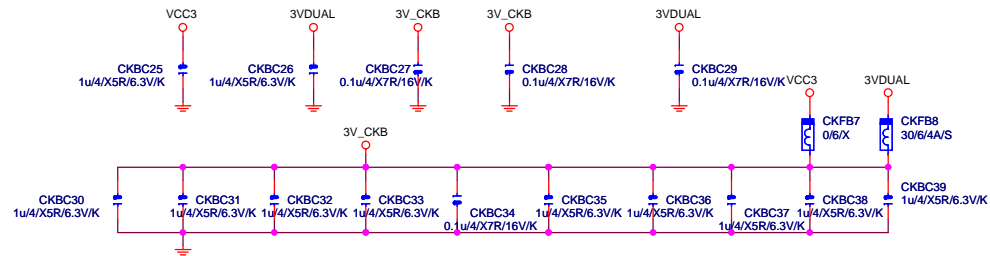
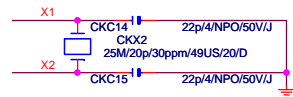
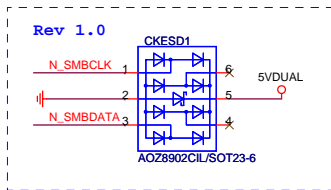
Document Number: **GA-X79-UD7**

Date: Wednesday, September 28, 2011

Sheet: 39 of 54

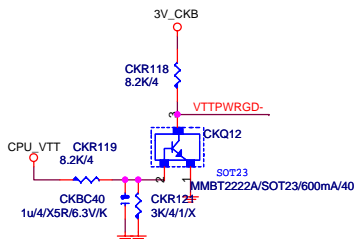
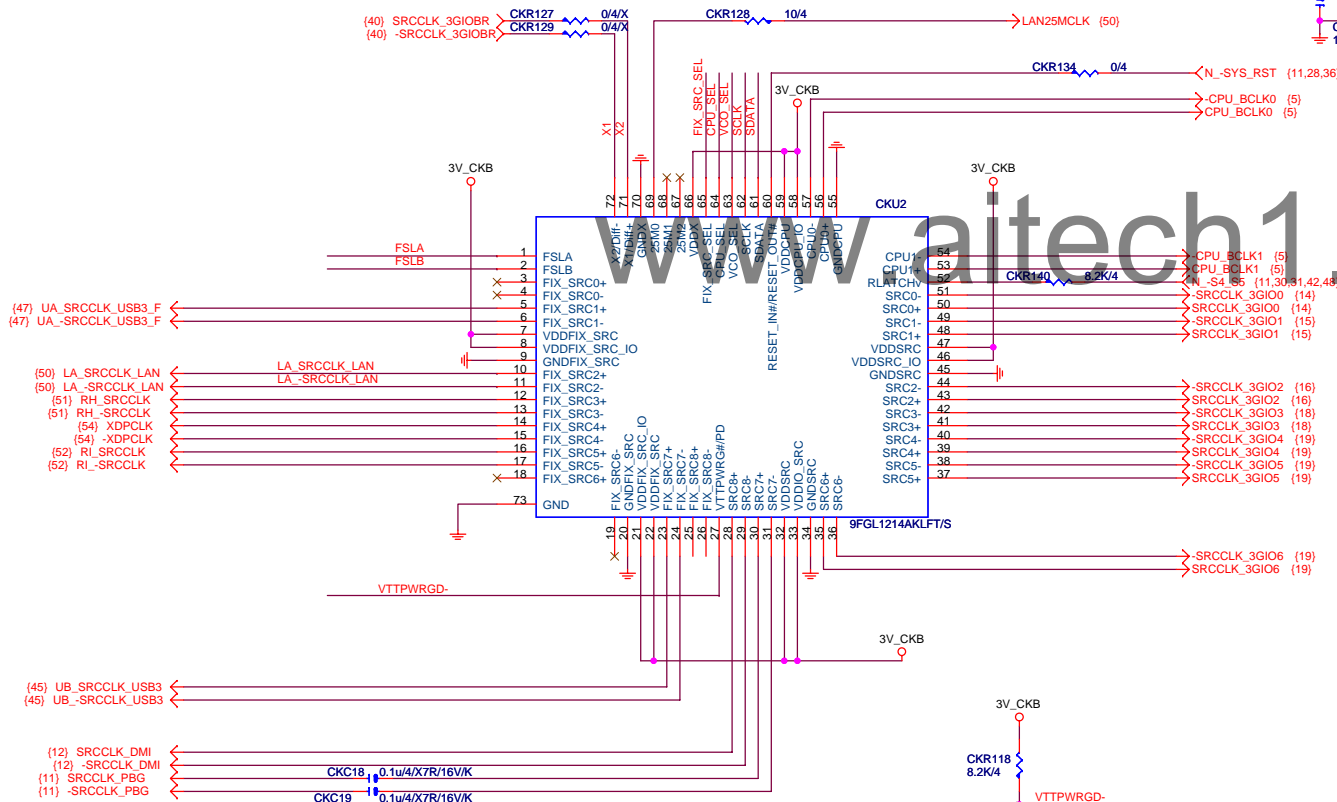
Rev: **1.01**





Latch when VTTTPWRGD low to 1.1V

FSLB	FSLA	BCLK
1	1	100M
1	0	125M
0	1	166M
0	0	250M

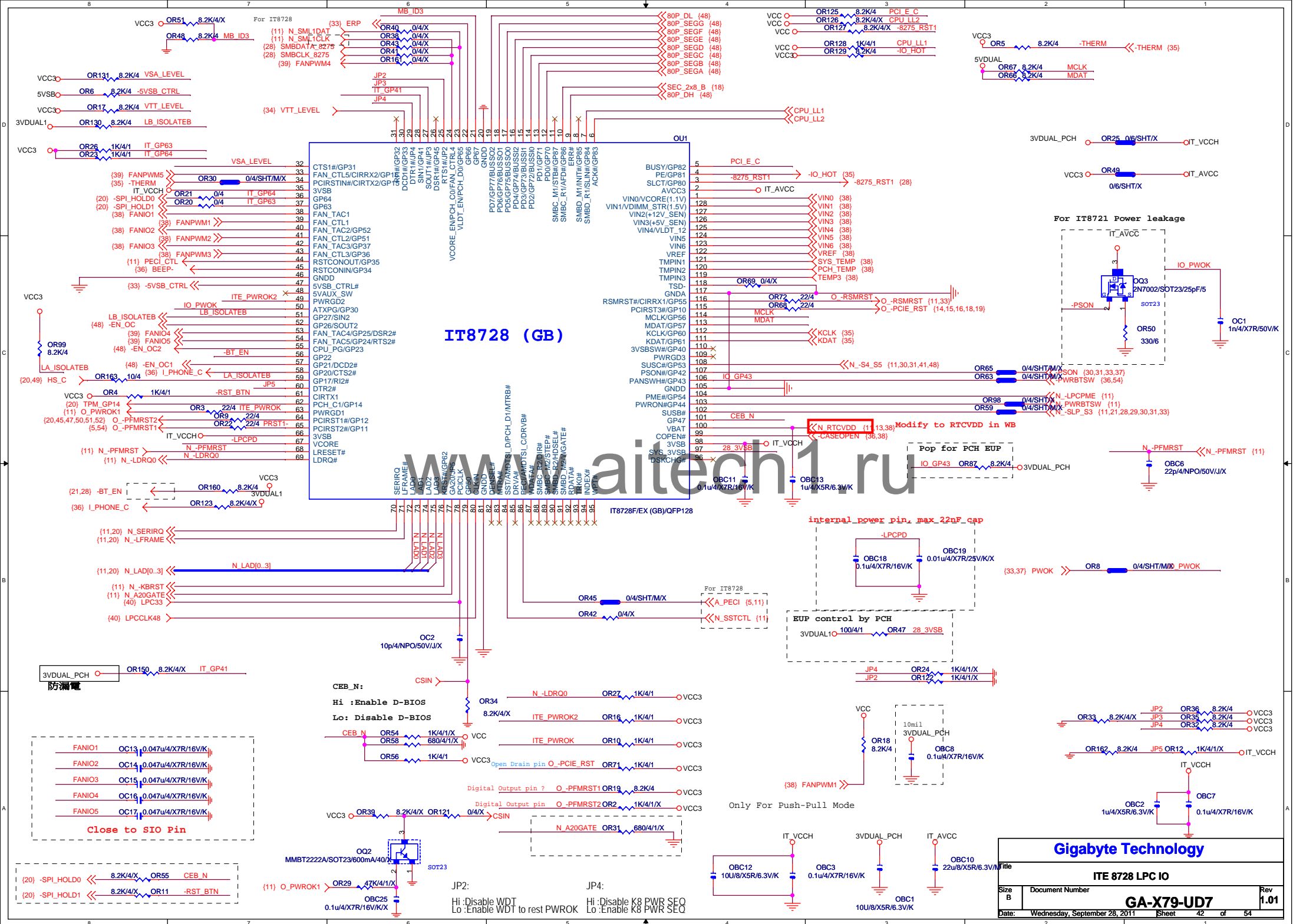


**GIGABYTE™**

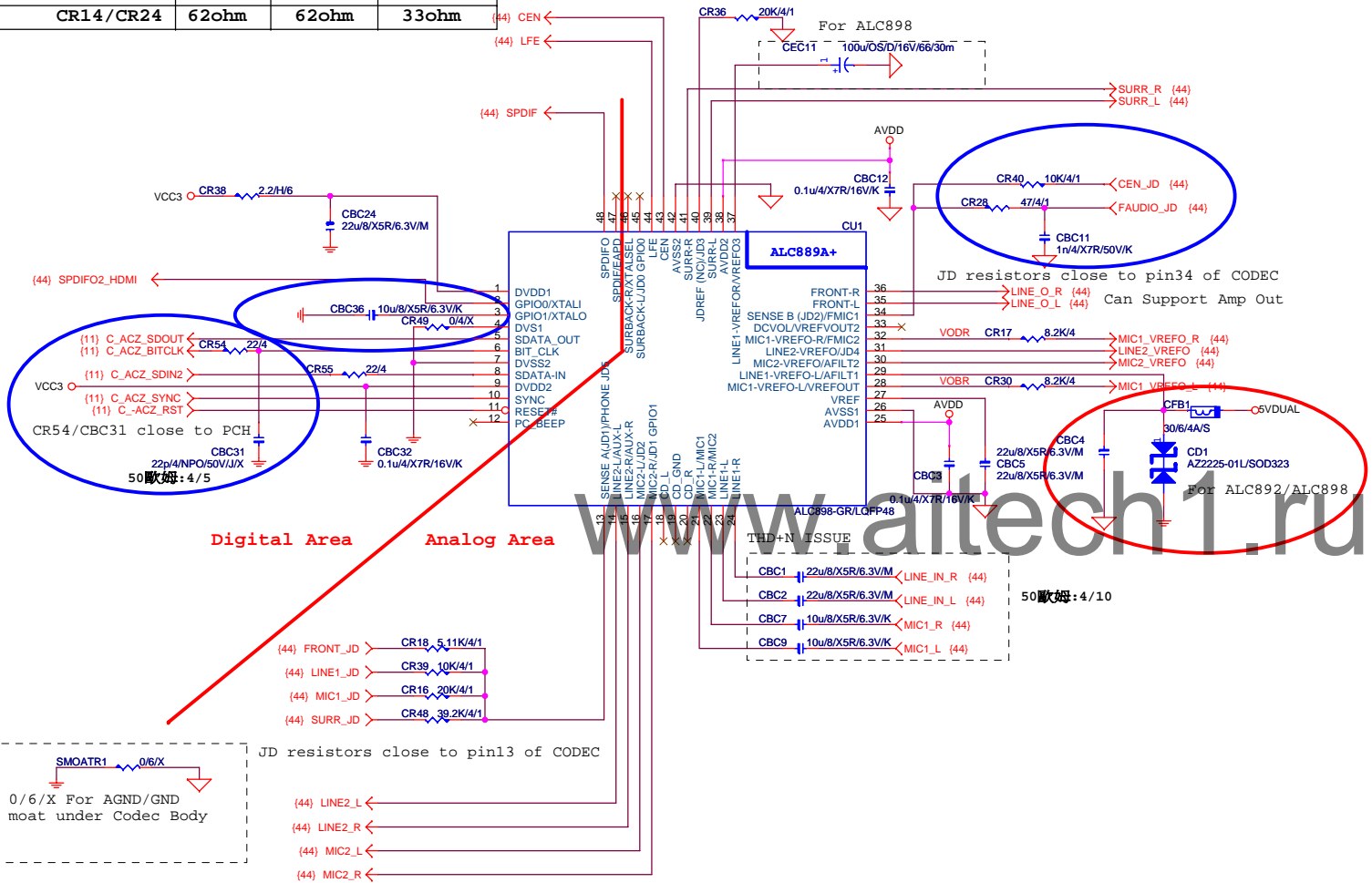
Title: **CLK BUFFER**

Size: Custom Document Number: **GA-X79-UD7** Rev: **1.01**

Date: Wednesday, September 28, 2011 Sheet: 41 of 54

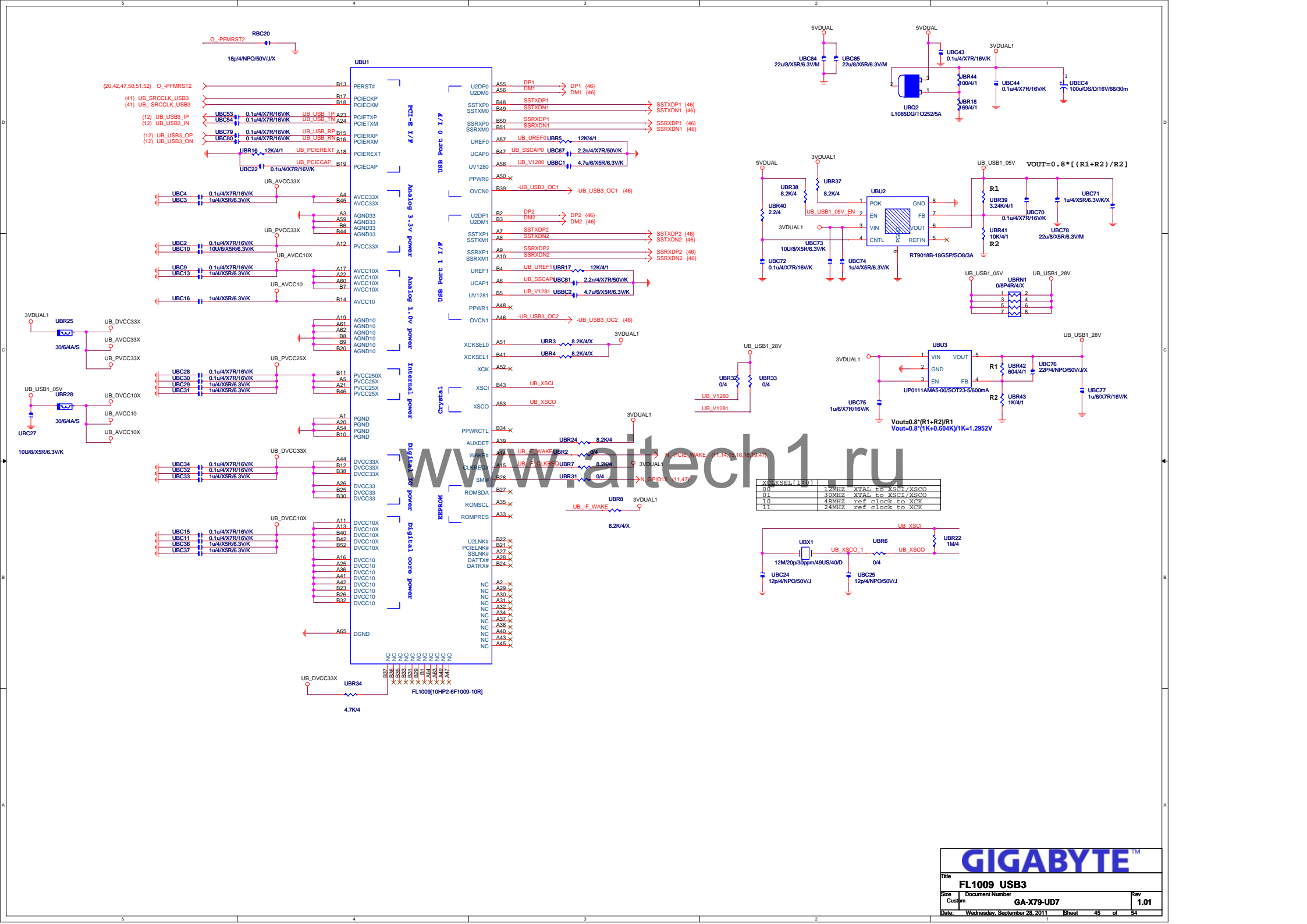


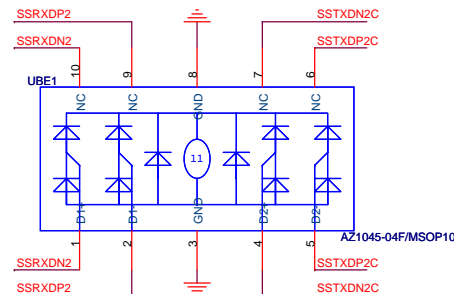
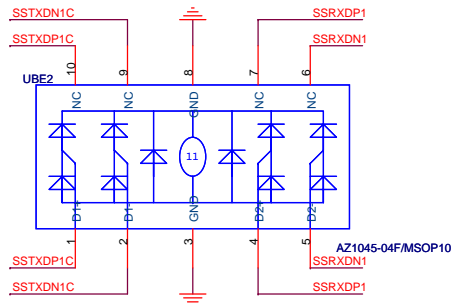
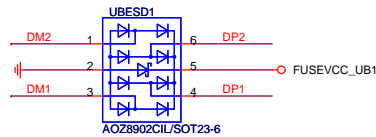
	ALC889	ALC892	ALC898
CBC36	X	10uF/X5R	10uF/X5R
CR49	O	X	X
CFB1/CD1/CBC4	X	O	O
CD2/CD3/CQ3/CQ4	O	X	X
CEC11	X	X	100uF
CR14/CR24	62ohm	62ohm	33ohm



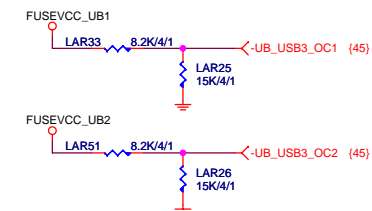
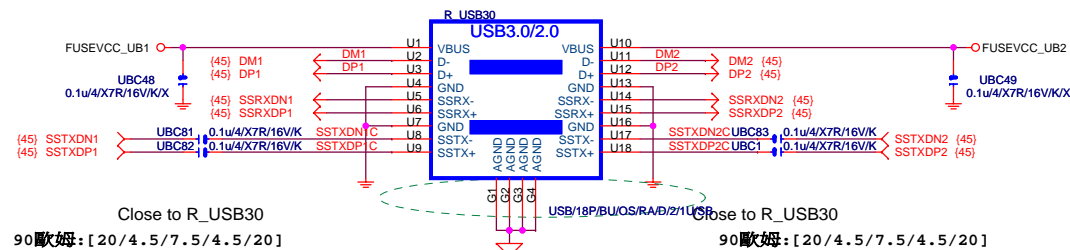
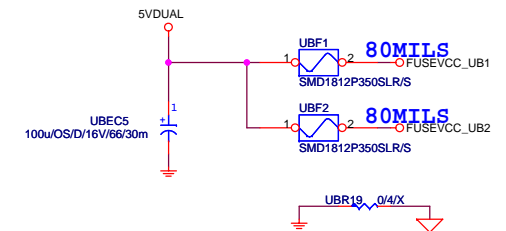


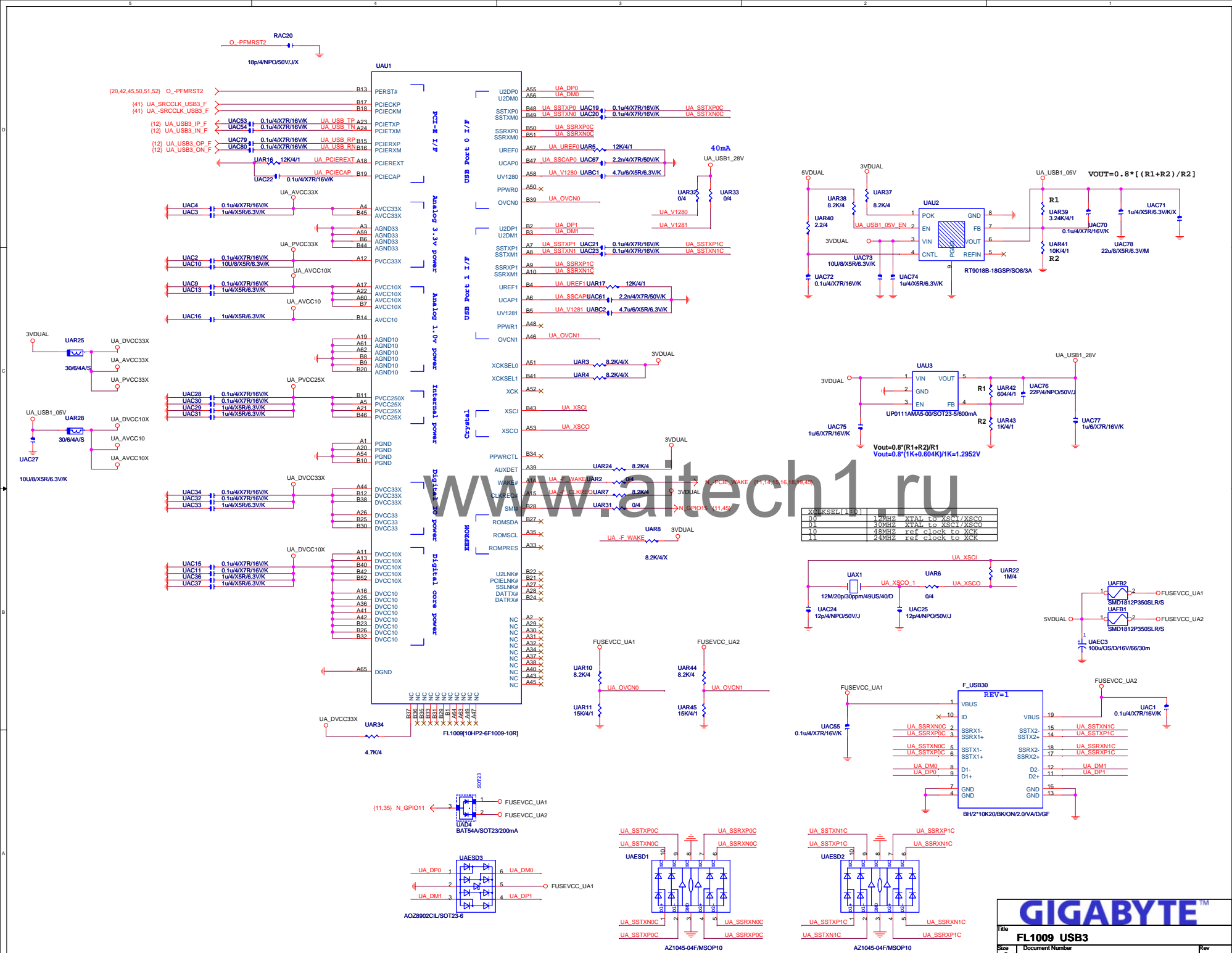


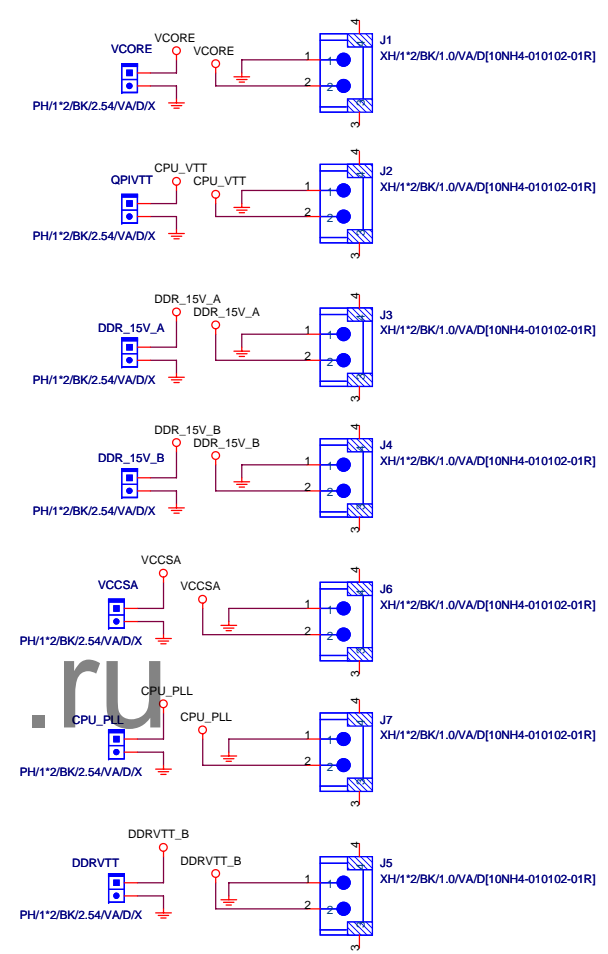
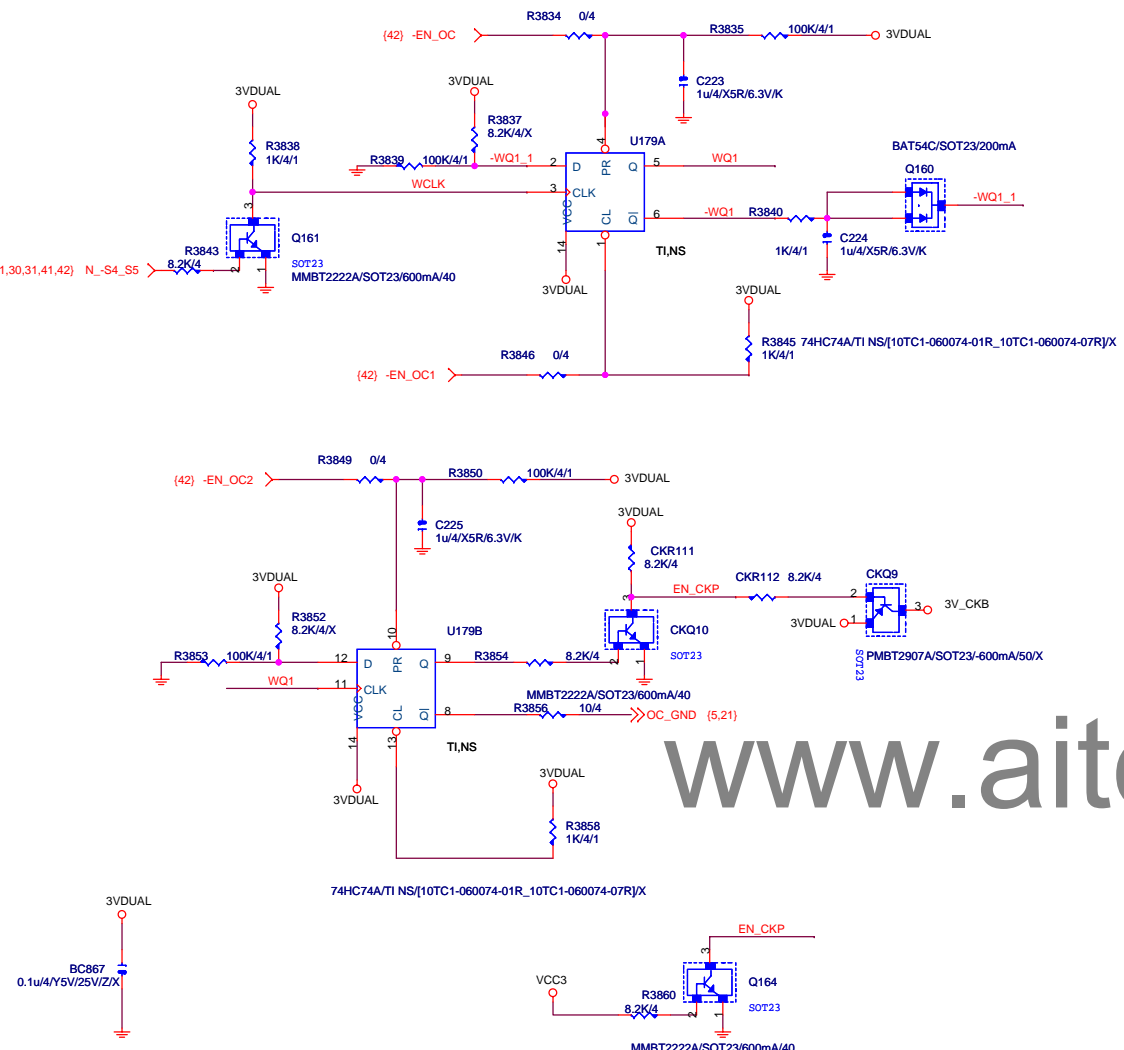




www.aitech1.ru

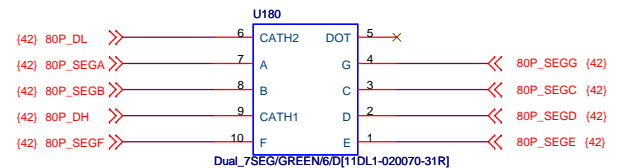
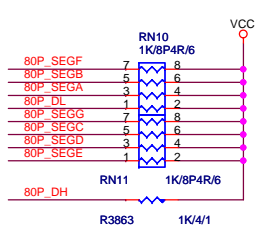




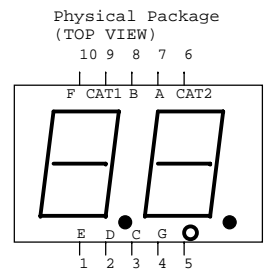


www.aitech1.ru

80 PORT



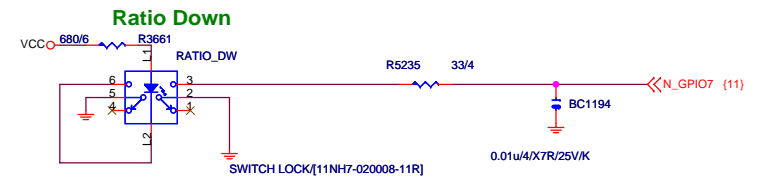
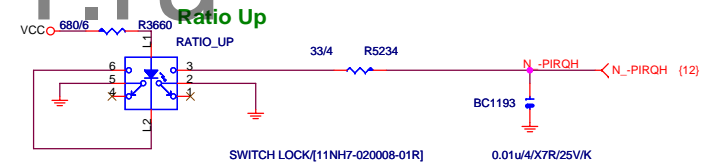
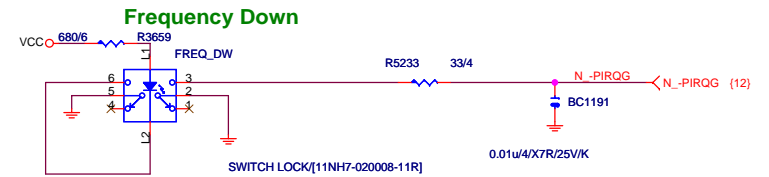
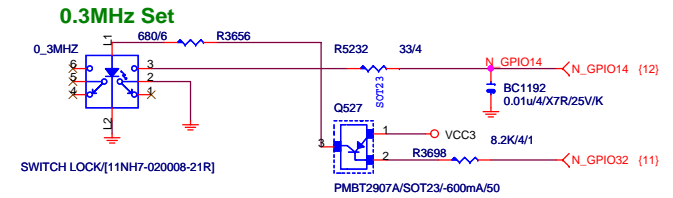
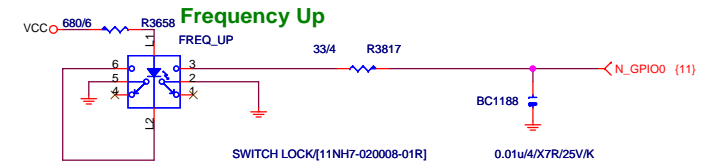
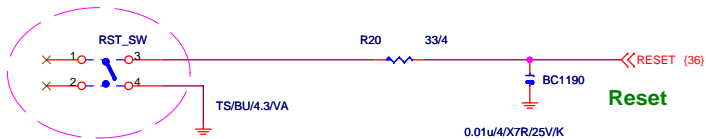
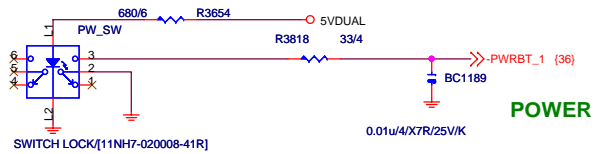
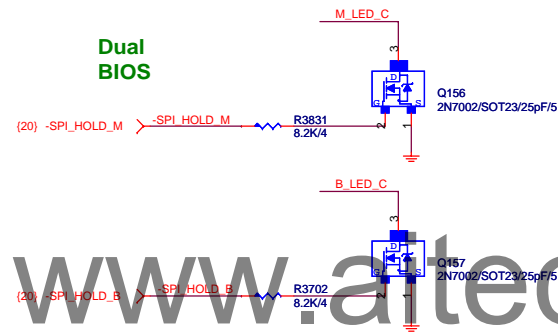
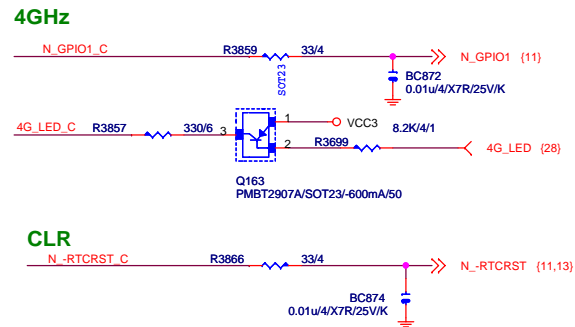
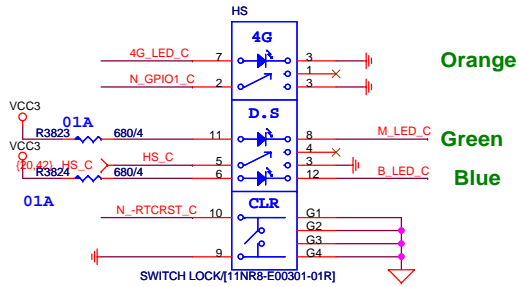
COMMON CATHODE



# GIGABYTE™

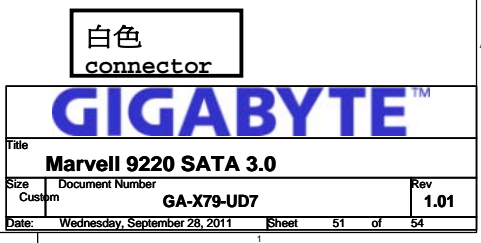
**Over Clock & 80 PORT**

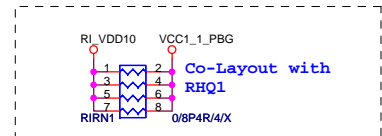
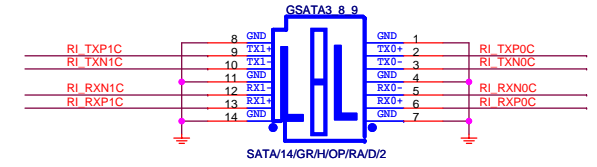
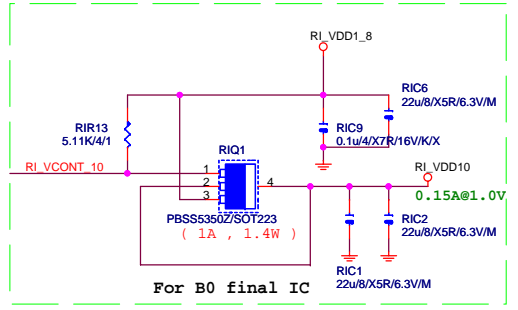
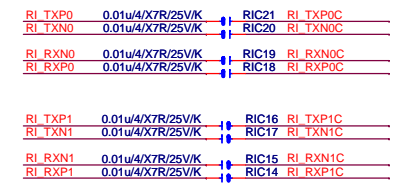
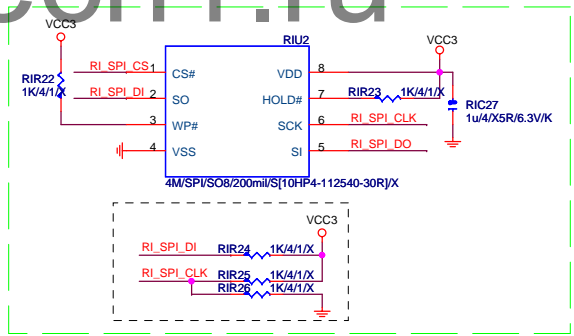
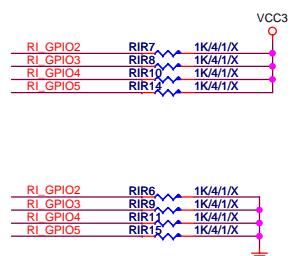
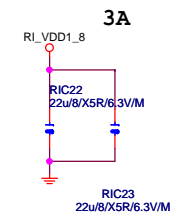
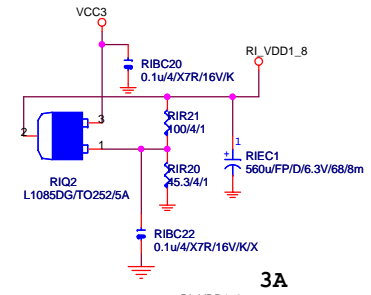
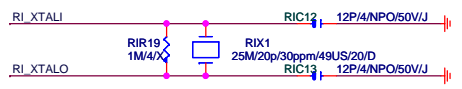
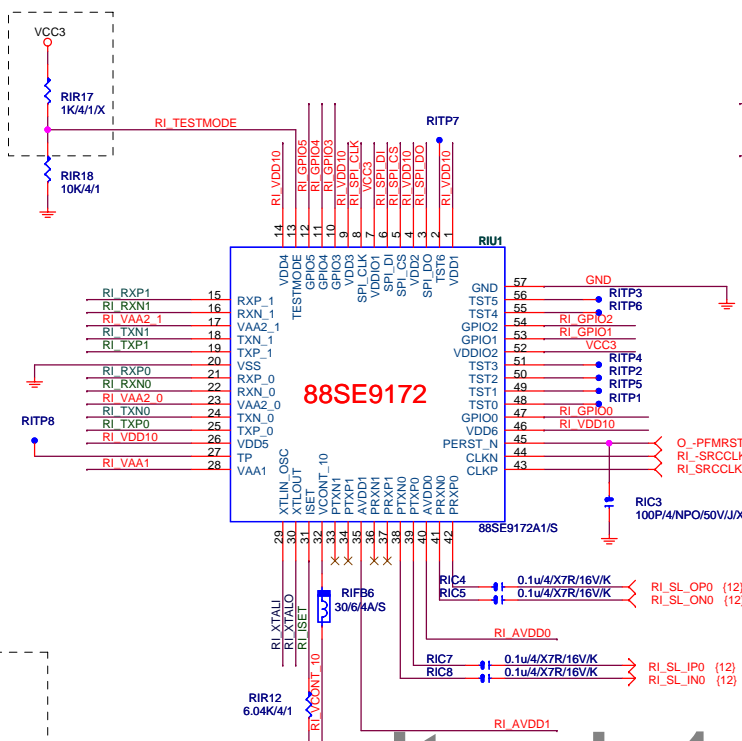
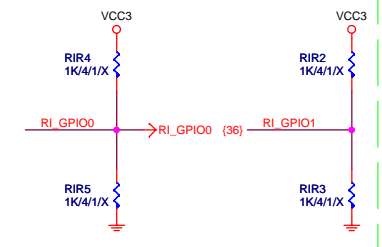
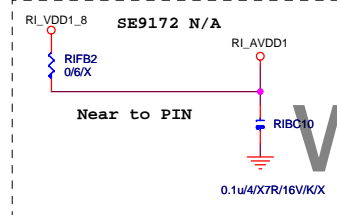
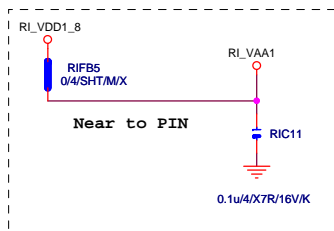
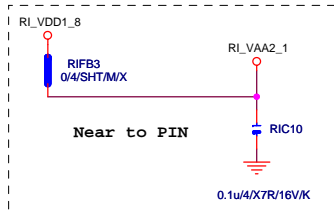
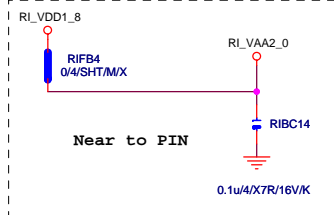
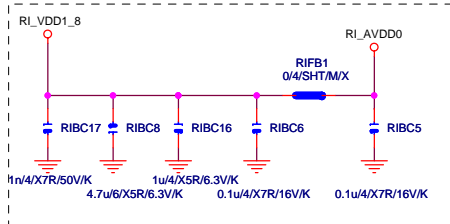
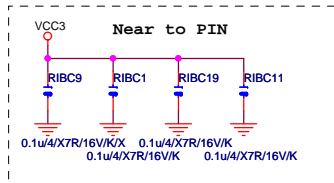
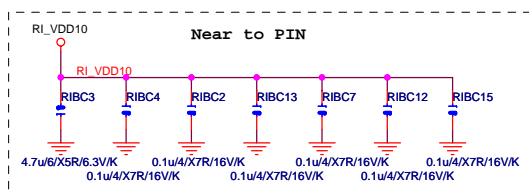
Title		Rev	
Document Number		1.01	
Custom		GA-X79-UD7	
Date:	Wednesday, September 28, 2011	Sheet	48 of 54











**GIGABYTE™**

**Marvell 9172/ 9220 SATA 3.0**

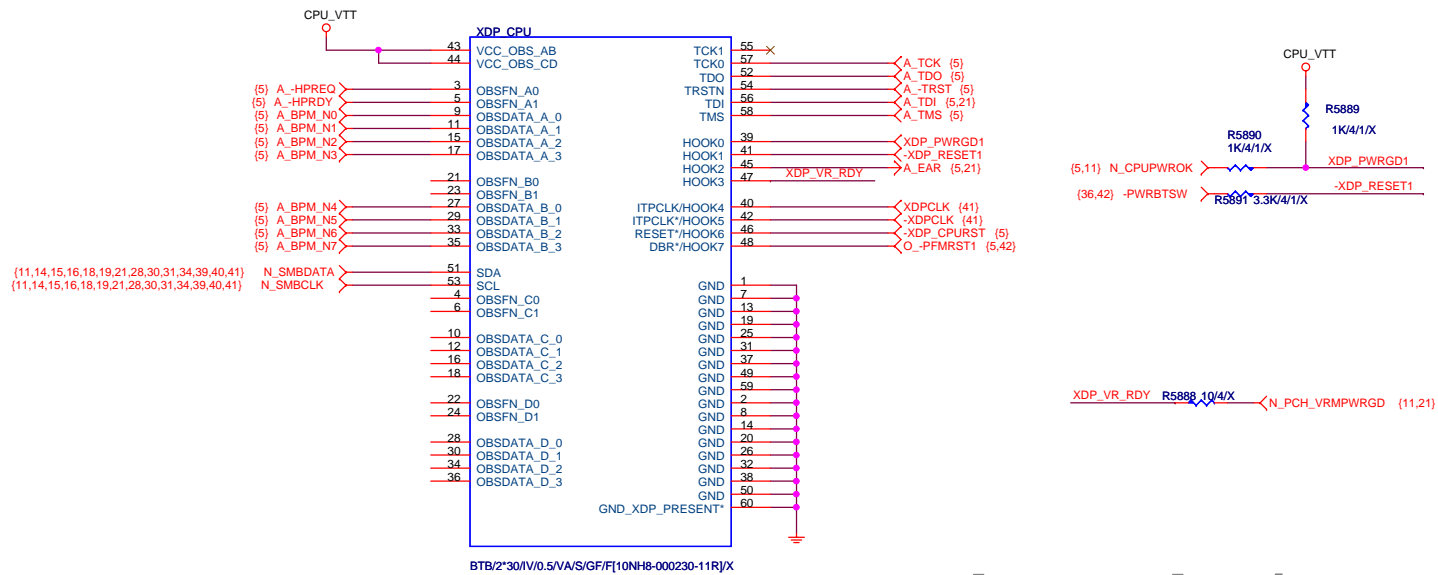
Size: Custom Document Number: **GA-X79-UD7** Rev: **1.01**

Date: Wednesday, September 28, 2011 Sheet: 52 of 54

PCH GPIO

PIN NAME	POWER WELL	USAGE	AFTER PLTRST	S3/S5	NOTES
GP[0]	VCC3	-ICH PSI	IN		8.2K P/U TO VCC3
GP[1]	VCC3	SPARE	IN		8.2K P/U TO VCC3
GP[2]	VCC3	-PIRQE	IN		8.2K P/U TO VCC3
GP[3]	VCC3	-PIRQF	IN		8.2K P/U TO VCC3
GP[4]	VCC3	-PIRQG	IN		8.2K P/U TO VCC3
GP[5]	VCC3	-PIRQH	IN		8.2K P/U TO VCC3
GP[6]	VCC3	GPIO6	IN		8.2K P/U TO VCC3
GP[7]	VCC3	GPIO7	IN		8.2K P/U TO VCC3
GP[8]	3VDUAL	GPIO8	OUT		8.2K P/U TO 3VDUAL
GP[9]	3VDUAL	-USBOC5	IN		USB OVER-CURRENT
GP[10]	3VDUAL	-USBOC6	IN		USB OVER-CURRENT
GP[11]	3VDUAL	GPIO11	IN		8.2K P/U TO 3VDUAL
GP[12]	3VDUAL	GPIO12	OUT		8.2K P/U TO 3VDUAL
GP[13]	3VDUAL	-LPCPME	IN		8.2K P/U TO 3VDUAL
GP[14]	3VDUAL	GPIO14	IN		8.2K P/U TO 3VDUAL
GP[15]	3VDUAL	SPARE	OUT		8.2K P/U TO 3VDUAL (N/A)
GP[16]	VCC3	SPARE	IN		8.2K P/U TO VCC3
GP[17]	VCC3	SPARE	IN		8.2K P/U TO VCC3
GP[18]	VCC3	-SPI_WP0	OUT		8.2K P/U TO VCC3
GP[19]	VCC3	SPARE	OUT		8.2K P/U TO VCC3
GP[20]	VCC3	-SPI_WP1	OUT		8.2K P/U TO VCC3
GP[21]	VCC3	SPARE	IN		8.2K P/U TO VCC3
GP[22]	VCC3	SPARE	IN		1K P/U TO VCC3
GP[23]	VCC3	SPARE	IN		8.2K P/U TO VCC3
GP[24]	3VDUAL	-SKTOC	IN		8.2K P/U TO 3VDUAL (N/A)
GP[25]	3VDUAL	GPIO25	OUT		8.2K P/U TO 3VDUAL
GP[26]	3VDUAL	SPARE	OUT		8.2K P/U TO 3VDUAL
GP[27]	3VDUAL_PCH	SPARE	OUT		8.2K P/U TO 3VDUAL_PCH
GP[28]	3VDUAL	GPIO28	OUT		8.2K P/U TO 3VDUAL
GP[29]	3VDUAL	SPARE	OUT		8.2K P/U TO 3VDUAL (N/A)
GP[30]	3VDUAL	-S_WARN	OUT		CONNECT TO -S_ACK
GP[31]	3VDUAL_PCH	SPARE	IN		8.2K P/U TO 3VDUAL_PCH(N/A)
GP[32]	VCC3	SPARE	OUT		8.2K P/U TO VCC3
GP[33]	VCC3	SPARE	OUT		8.2K P/U TO VCC3
GP[34]	VCC3	SPARE	IN		8.2K P/U TO VCC3
GP[35]	VCC3	-ACZ_DET	OUT		8.2K P/U TO VCC3
GP[36]	VCC3	SPARE	IN		8.2K P/U TO VCC3(N/A)
GP[37]	VCC3	SPARE	IN		8.2K P/U TO VCC3
GP[38]	VCC3	SPARE	IN		1K P/U TO VCC3

PIN NAME	POWER WELL	USAGE	AFTER PLTRST	S3/S5	NOTES
GP[39]	VCC3	SPARE	IN		1K P/U TO VCC3
GP[40]	3VDUAL	-USBOC1	IN		USB OVER-CURRENT
GP[41]	3VDUAL	-USBOC2	IN		USB OVER-CURRENT
GP[42]	3VDUAL	-USBOC3	IN		USB OVER-CURRENT
GP[43]	3VDUAL	-USBOC4	IN		USB OVER-CURRENT
GP[44]	3VDUAL	SPARE	IN		1K P/U TO 3VDUAL
GP[45]	3VDUAL	SPARE	IN		1K P/U TO 3VDUAL
GP[46]	3VDUAL	SPARE	IN		1K P/U TO 3VDUAL
GP[47]	3VDUAL	SPARE	IN		1K P/U TO 3VDUAL
GP[48]	VCC3	SPARE	IN		1K P/U TO VCC3
GP[49]	VCC3	SPARE	IN		8.2K P/U TO VCC3
GP[50]	VCC3	-REQ1	OUT		8.2K P/U TO VCC3
GP[51]	VCC3	-GNT1	OUT		1K P/U TO VCC3
GP[52]	VCC3	-REQ2	OUT		8.2K P/U TO VCC3
GP[53]	VCC3	-GNT2	IN		8.2K P/U TO VCC3(N/A)
GP[54]	VCC3	-REQ3	IN		8.2K P/U TO VCC3
GP[55]	VCC3	-GNT3	IN		8.2K P/U TO VCC3(N/A)
GP[56]	3VDUAL	SPARE	IN		8.2K P/U TO 3VDUAL
GP[57]	3VDUAL	SPARE	IN		8.2K P/U TO 3VDUAL
GP[58]	3VDUAL	SML1CLK	OUT		8.2K P/U TO 3VDUAL
GP[59]	3VDUAL	-USBOC0	IN		USB OVER-CURRENT
GP[60]	3VDUAL	SML0ART	OUT		1K P/U TO 3VDUAL
GP[61]	3VDUAL	SPARE	OUT		8.2K P/U TO 3VDUAL
GP[62]	3VDUAL	SUSCLK	OUT		8.2K P/U TO 3VDUAL(N/A)
GP[63]	3VDUAL	-SLP_S5	OUT		8.2K P/U TO 3VDUAL(N/A)
GP[64]	VCC3	SPARE	OUT		8.2K P/U TO VCC3
GP[65]	VCC3	SPARE	OUT		8.2K P/U TO VCC3
GP[66]	VCC3	SPARE	OUT		8.2K P/U TO VCC3
GP[67]	VCC3	SPARE	OUT		8.2K P/U TO VCC3
GP[68]	VCC3	SPARE	OUT		8.2K P/U TO VCC3
GP[69]	VCC3	SPARE	OUT		8.2K P/U TO VCC3
GP[70]	VCC3	SPARE	OUT		8.2K P/U TO VCC3
GP[71]	VCC3	SPARE	OUT		8.2K P/U TO VCC3
GP[72]	3VDUAL	SPARE	OUT		8.2K P/U TO 3VDUAL
GP[73]	3VDUAL	SPARE	OUT		8.2K P/U TO 3VDUAL
GP[74]	3VDUAL	SML1ART	OUT		1K P/U TO 3VDUAL
GP[75]	3VDUAL	SML1DAT	IN/OUT		8.2K P/U TO 3VDUAL



www.aitech1.ru